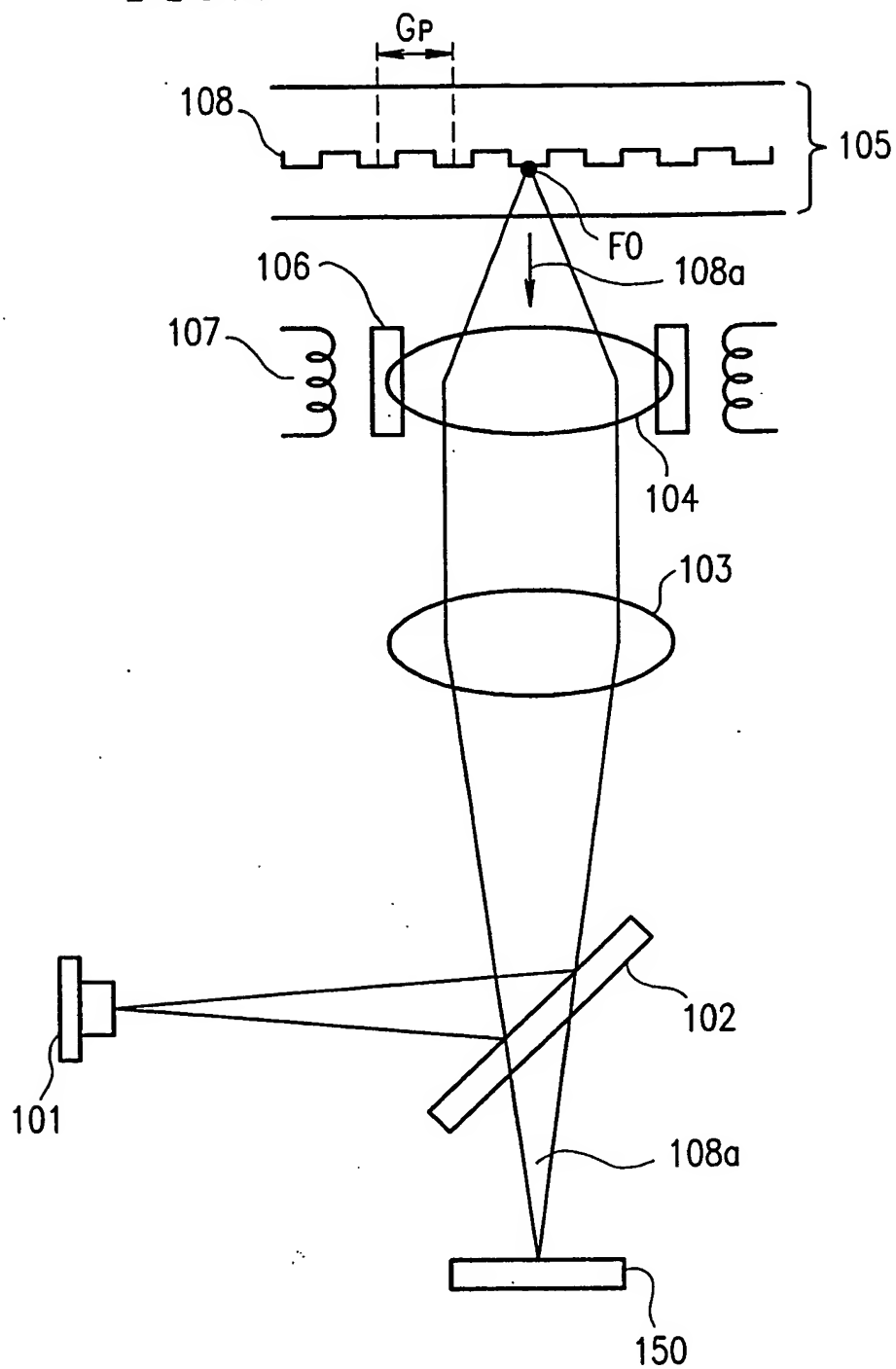
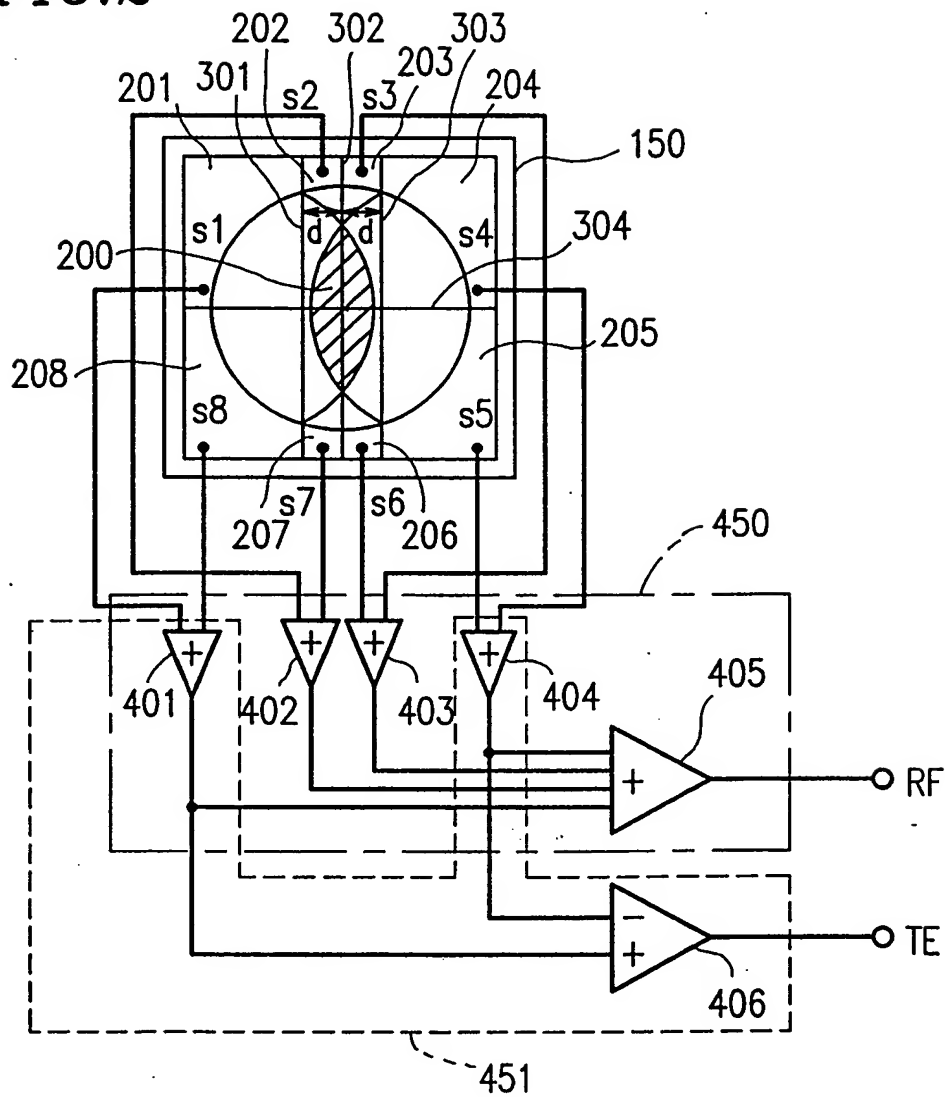


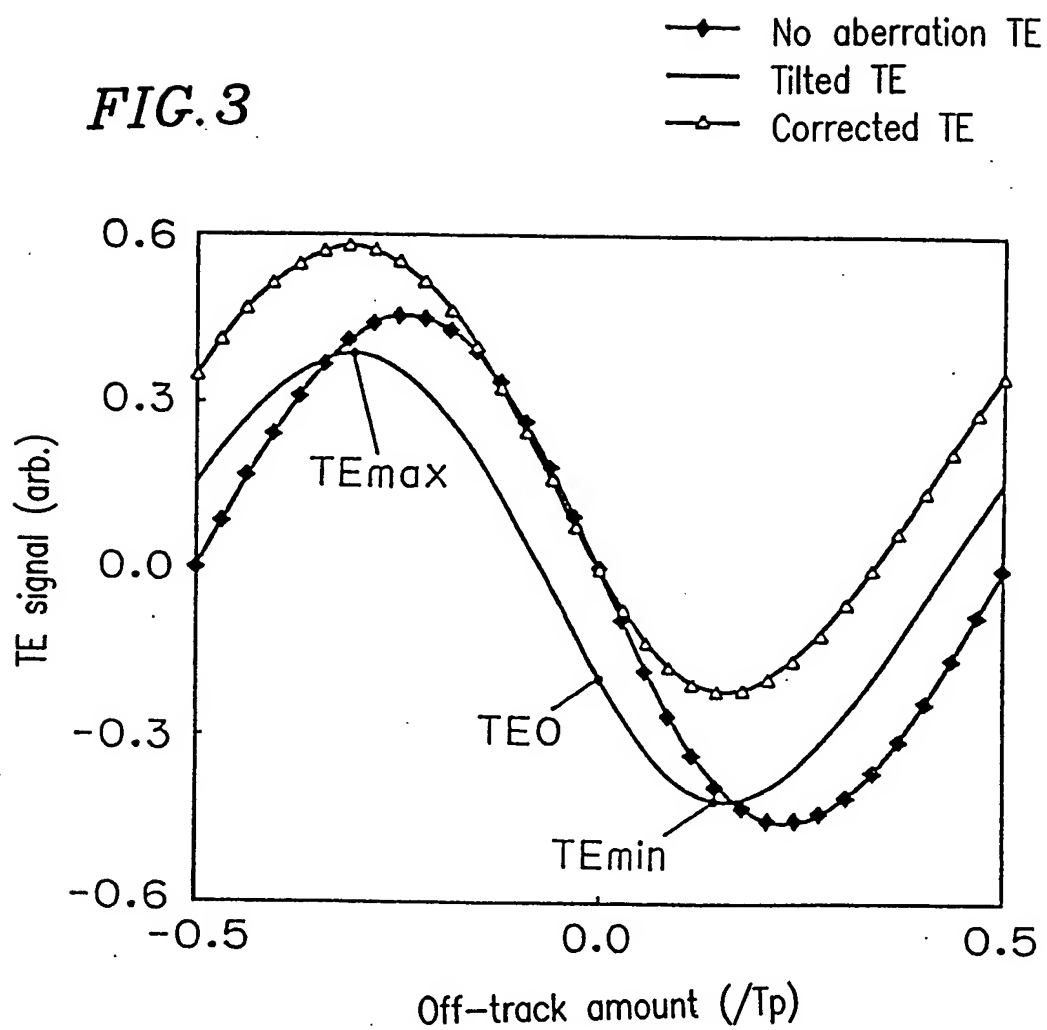
**FIG. 1**



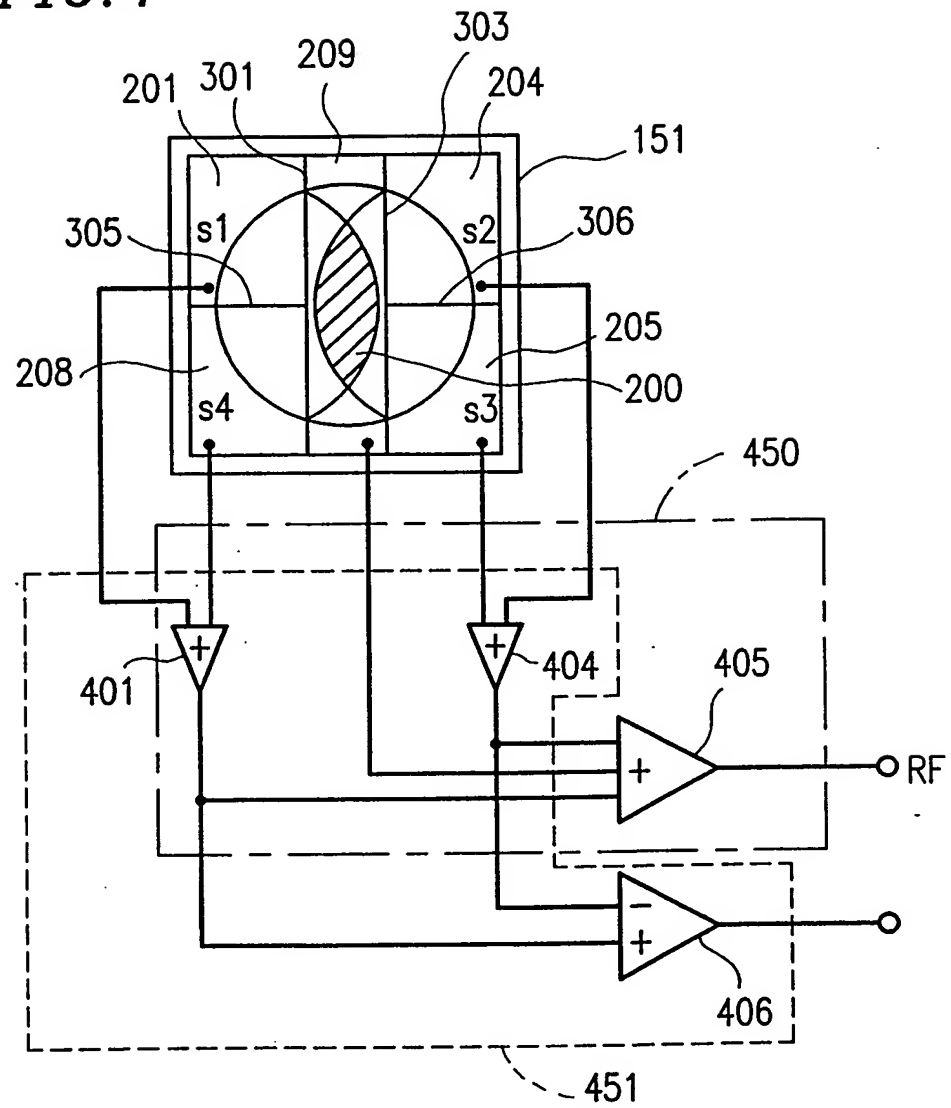
**FIG. 2**



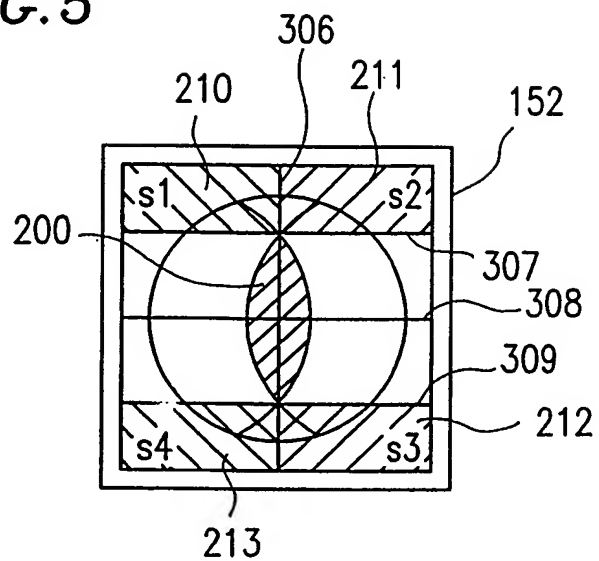
**FIG. 3**



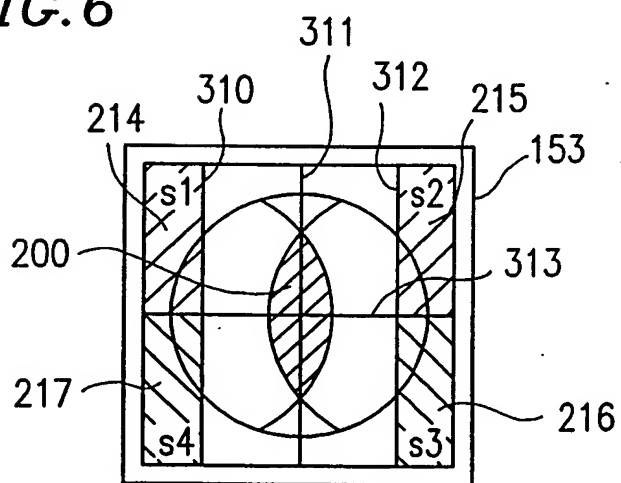
**FIG. 4**



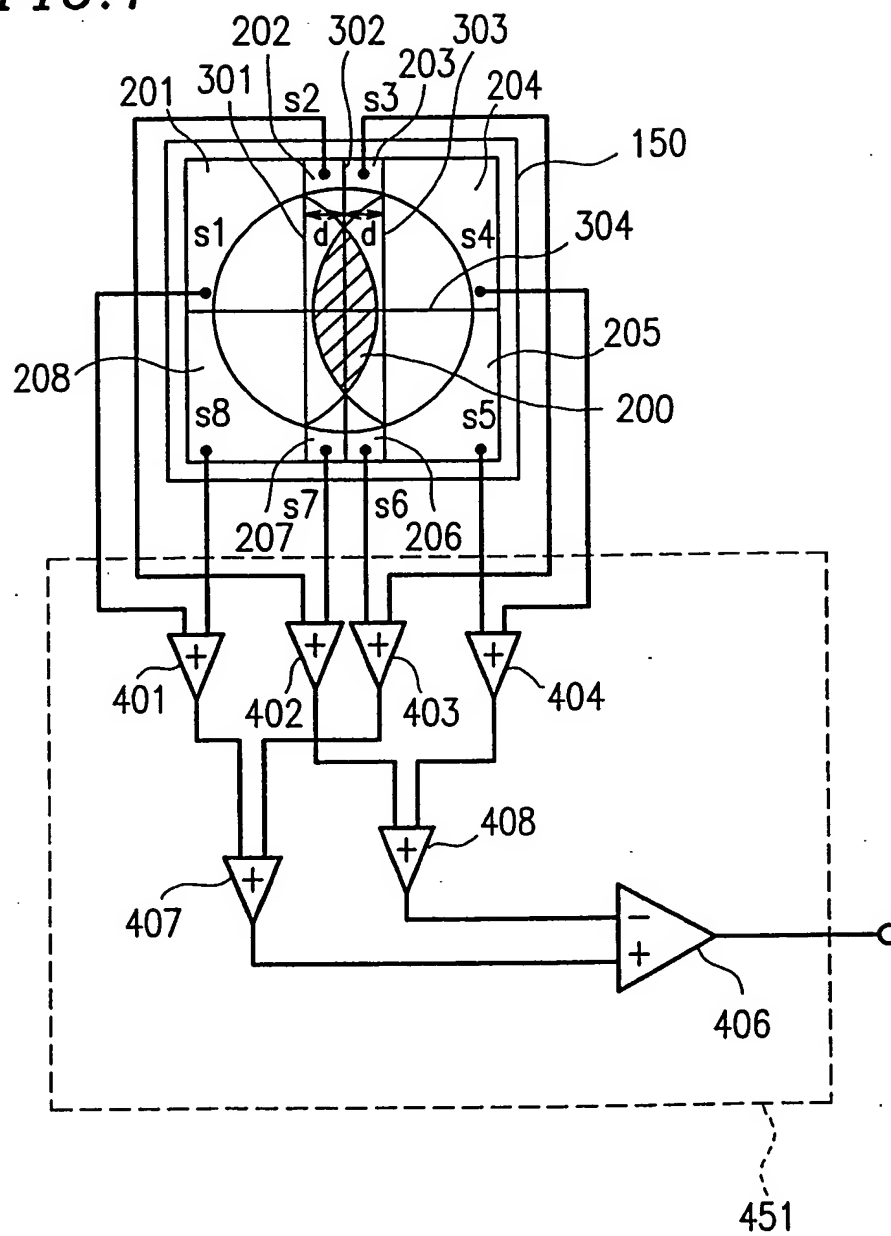
**FIG. 5**



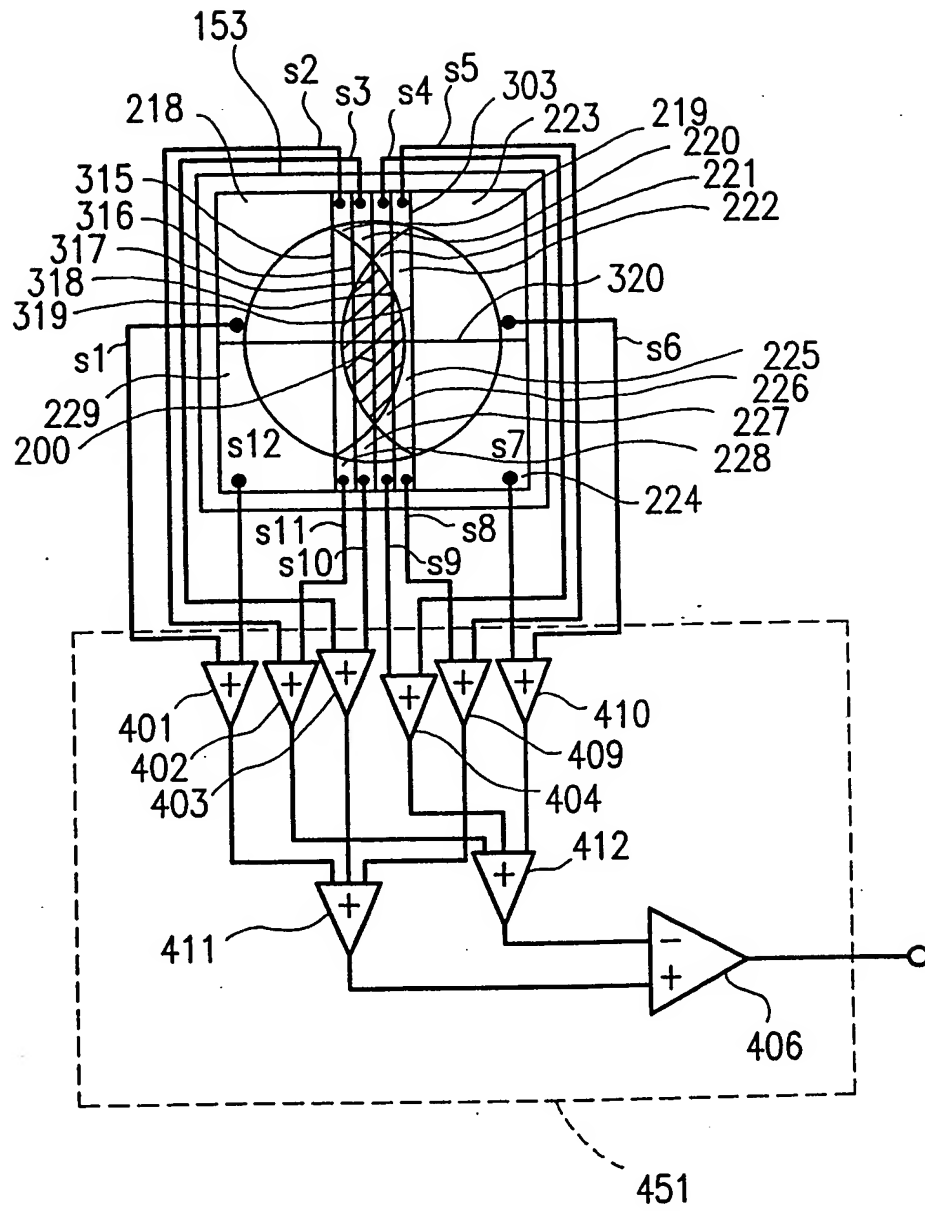
**FIG. 6**



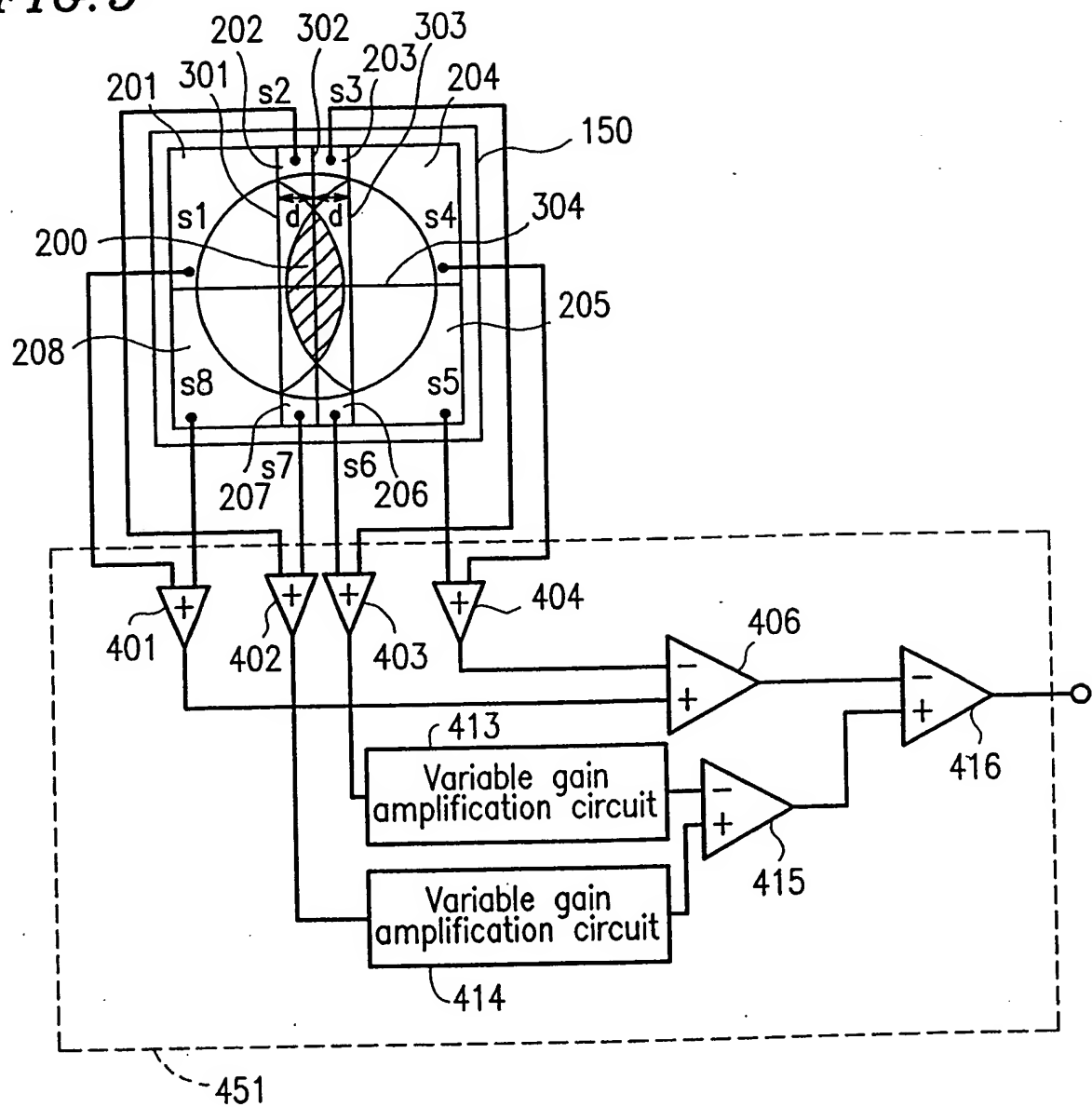
**FIG. 7**



**FIG. 8**

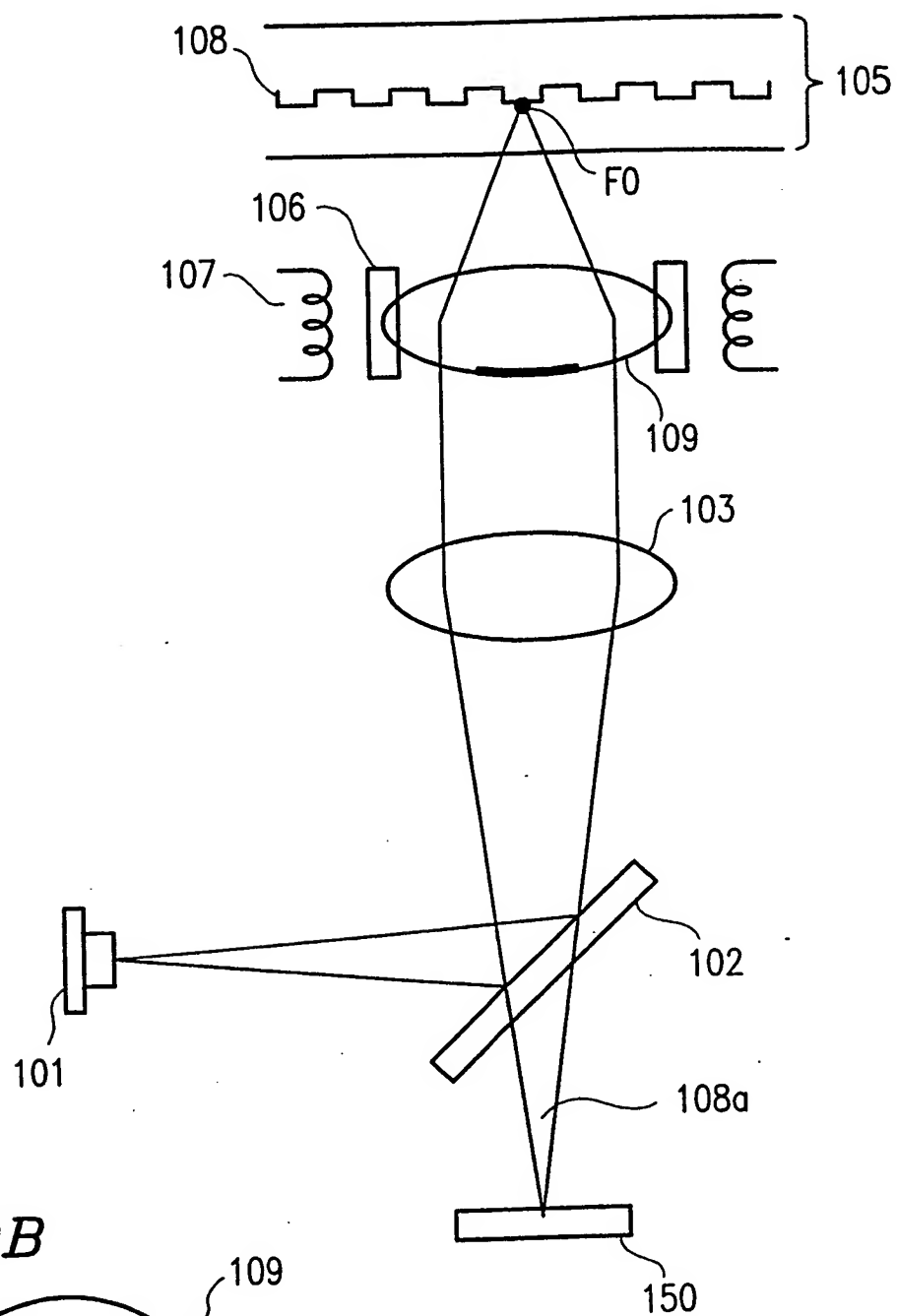


**FIG. 9**

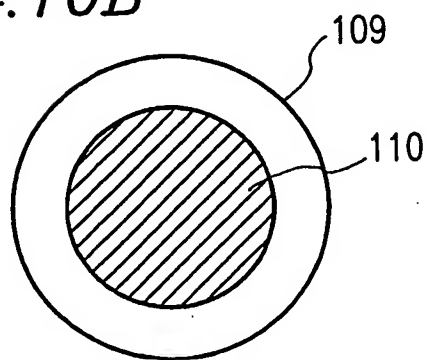




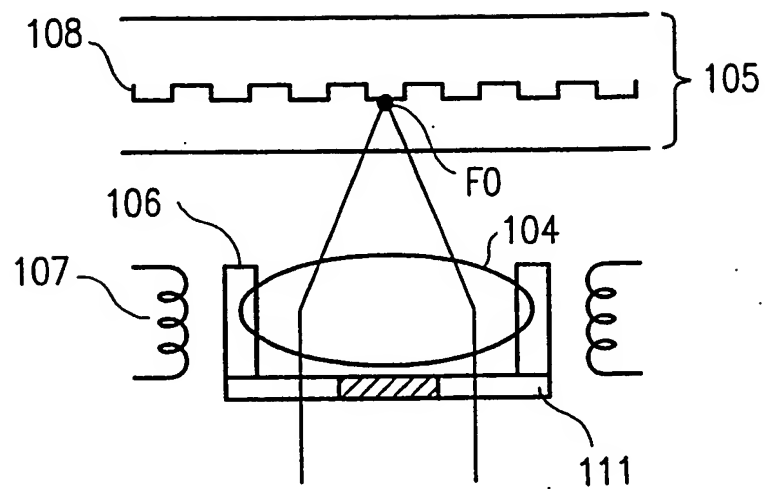
**FIG. 10A**



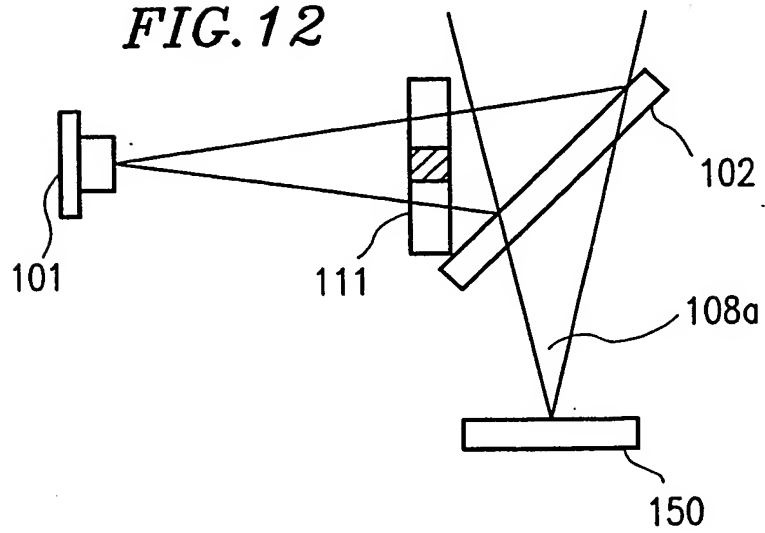
**FIG. 10B**



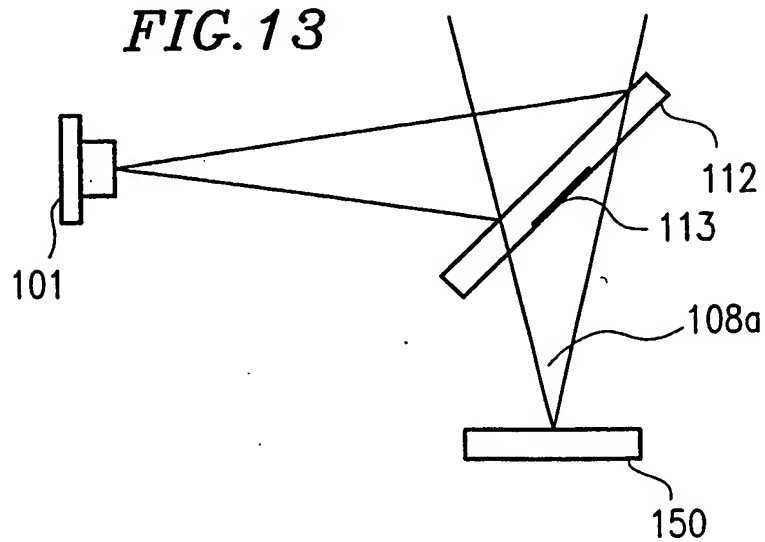
**FIG. 11**



**FIG. 12**



**FIG. 13**



**FIG. 14**

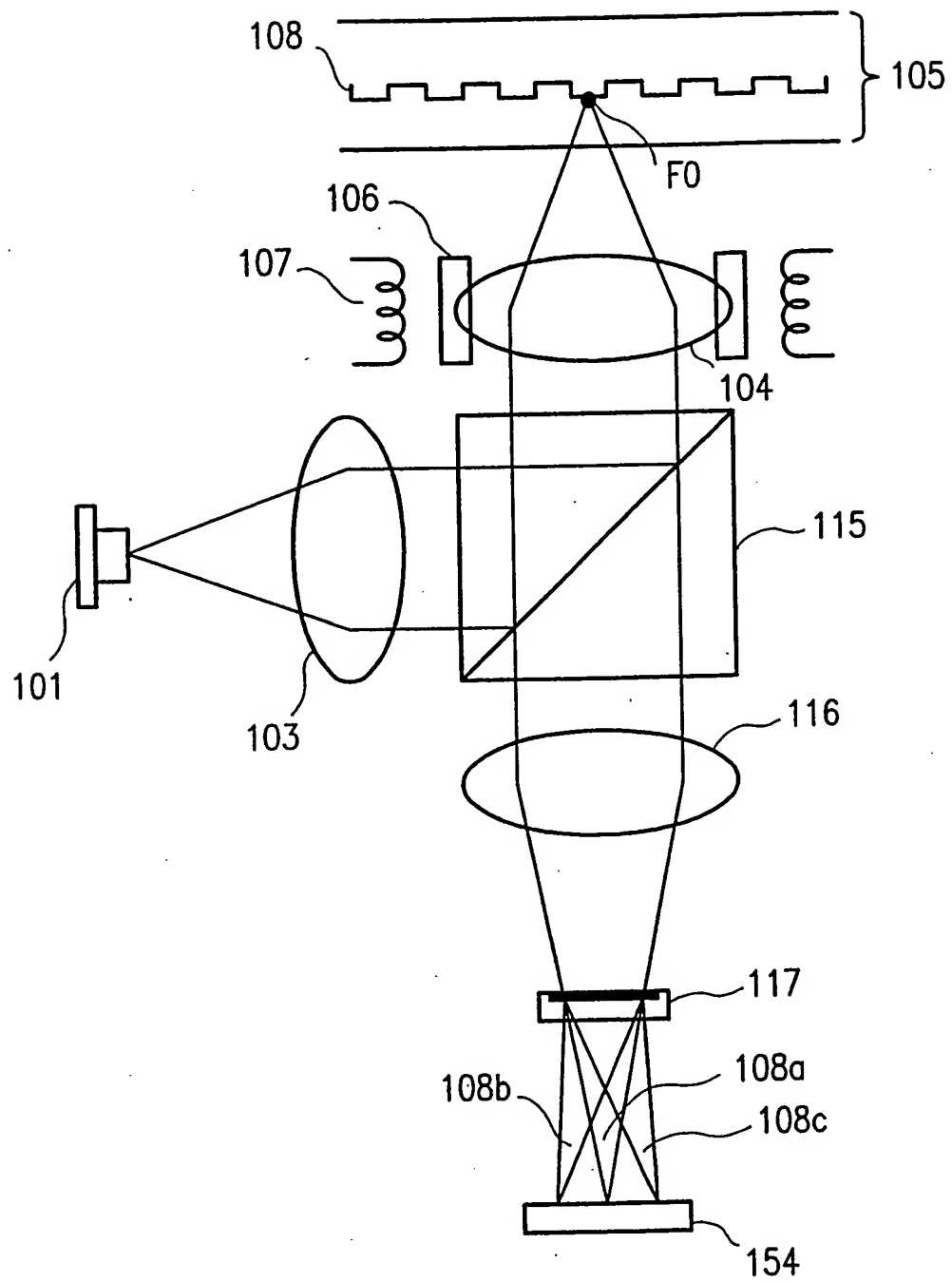
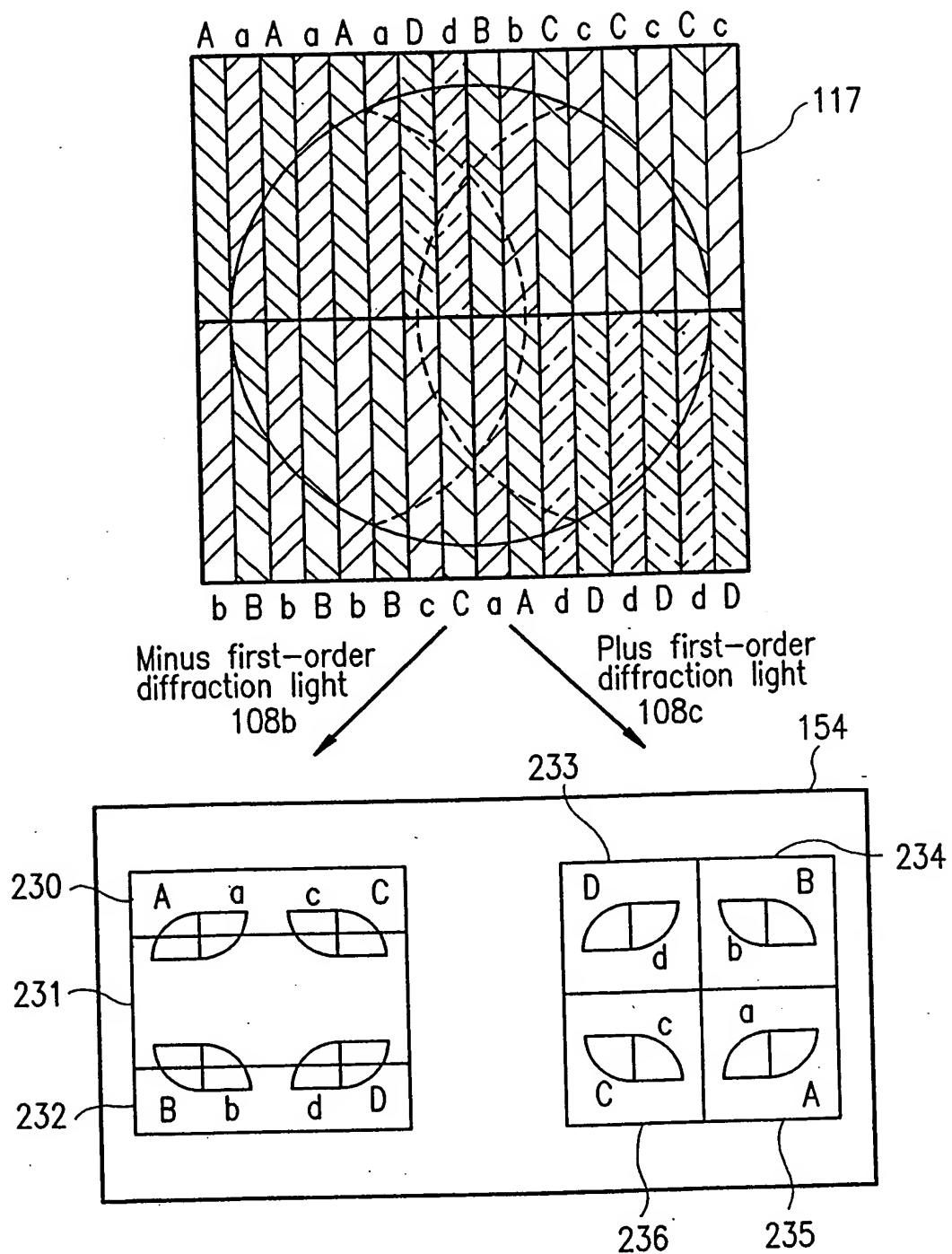
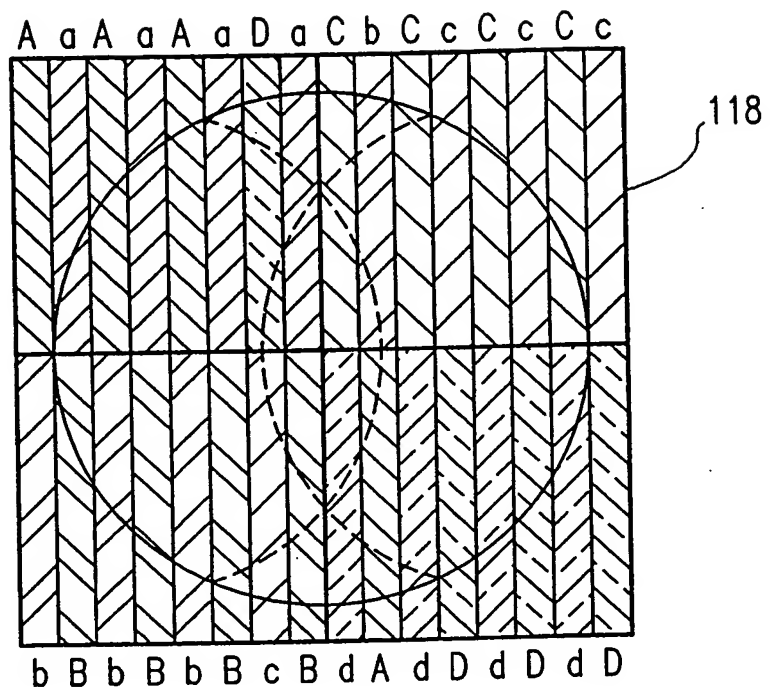


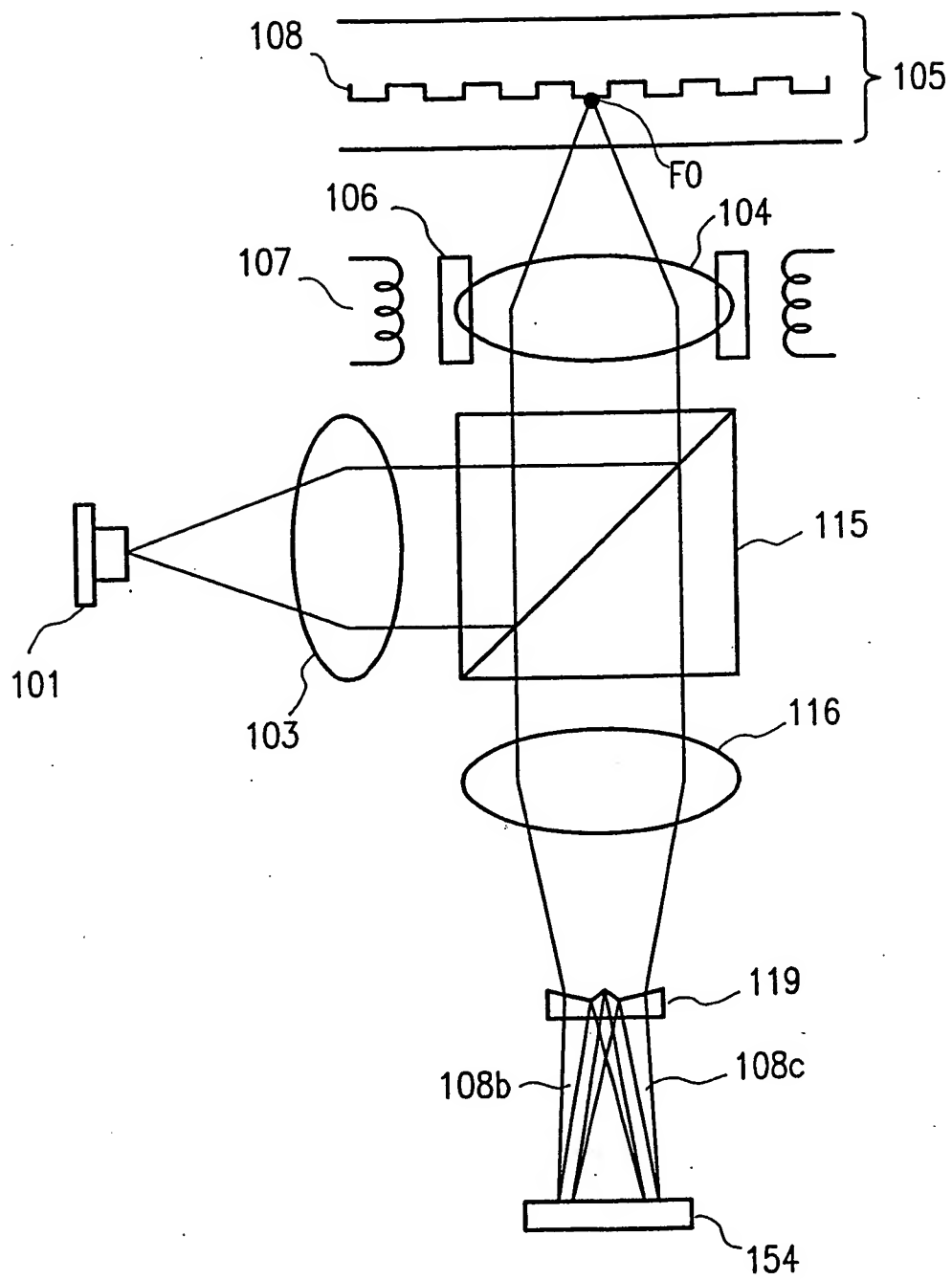
FIG. 15



**FIG. 16**



**FIG. 17**



**FIG. 18**

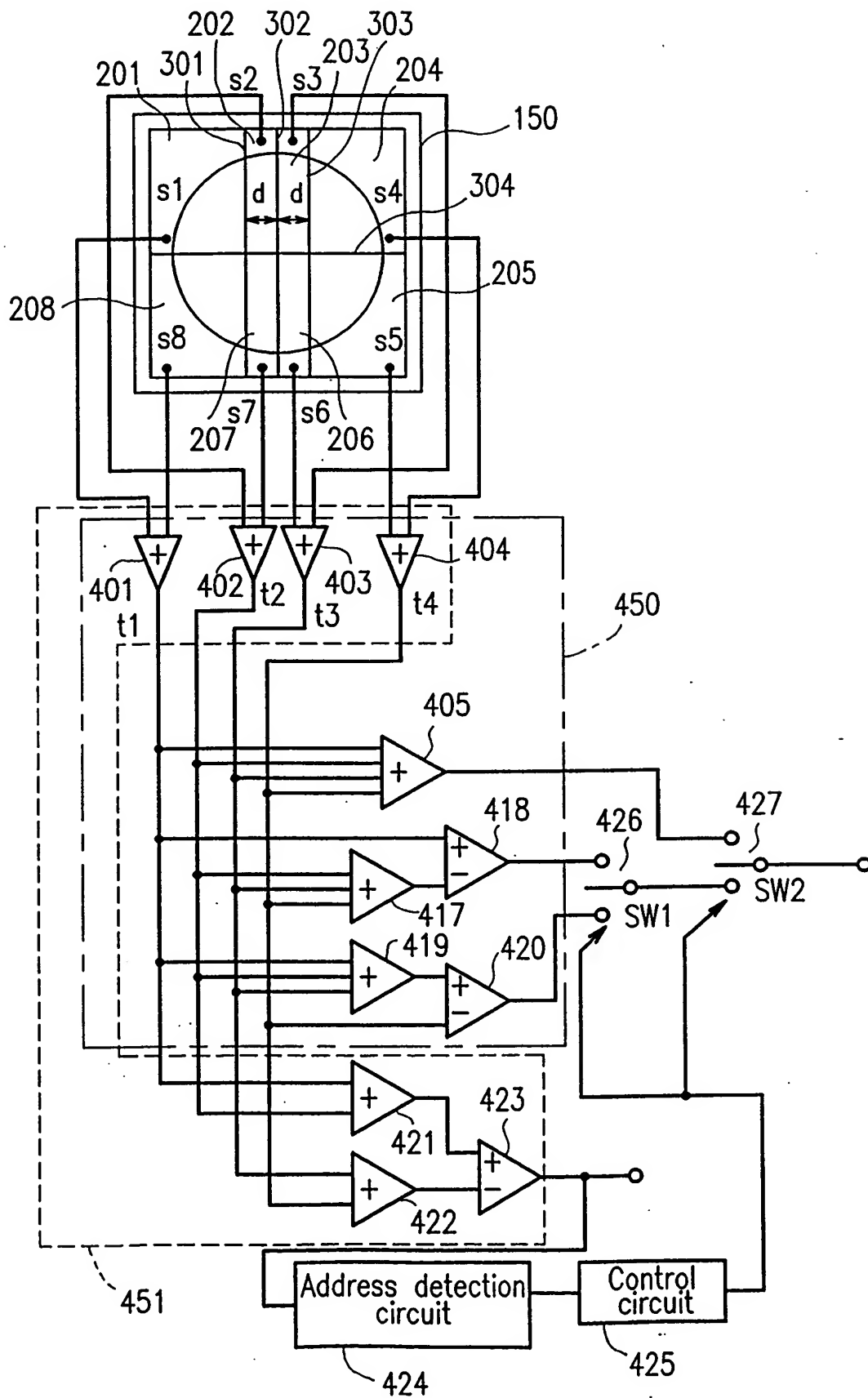
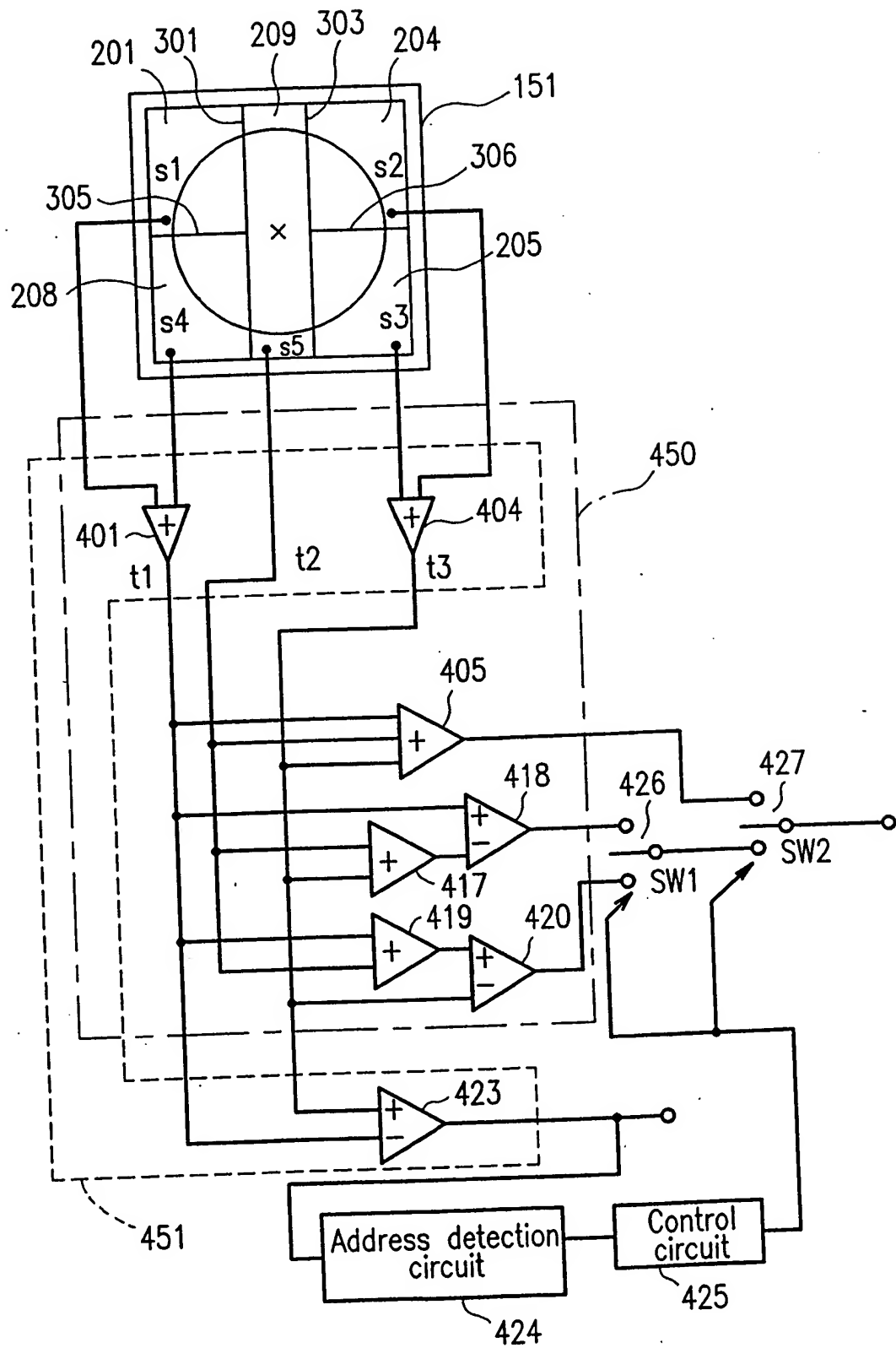
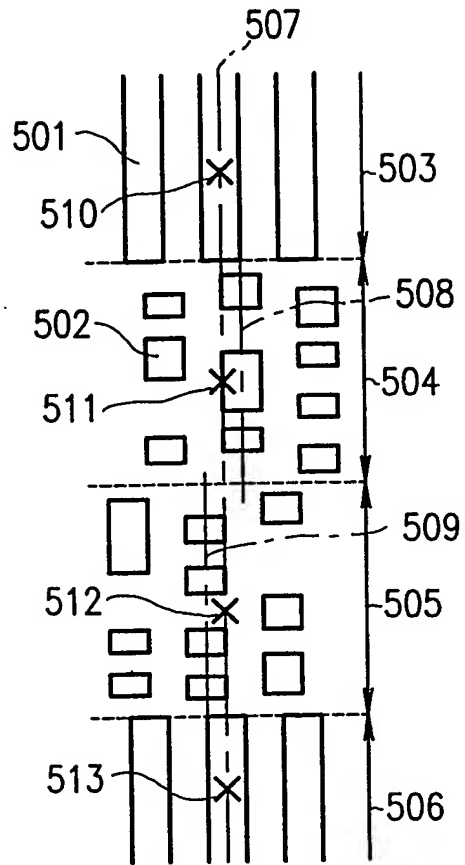
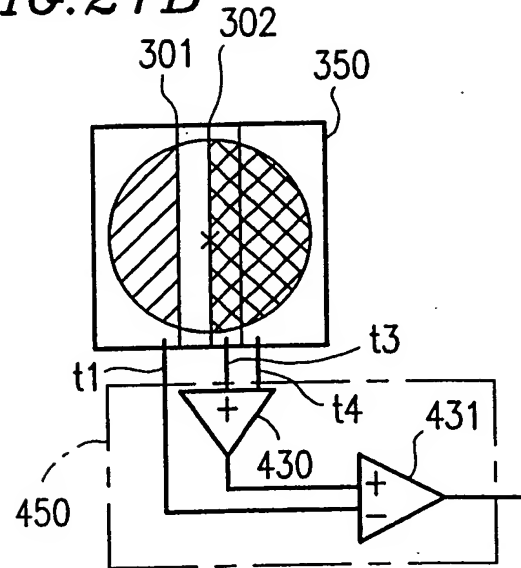
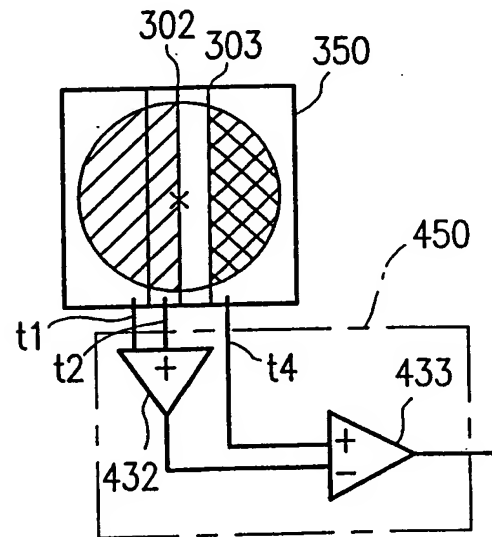


Figure 1 is a schematic diagram of a multi-layered structure, possibly a semiconductor device or a filter. The structure is divided into three horizontal sections by dashed lines. The top section contains vertical bars (501, 503, 507) and a cross (510). The middle section contains rectangular blocks (502, 504, 508) and a cross (511). The bottom section contains rectangular blocks (512, 505, 509) and a cross (513). Arrows indicate vertical flow between sections.

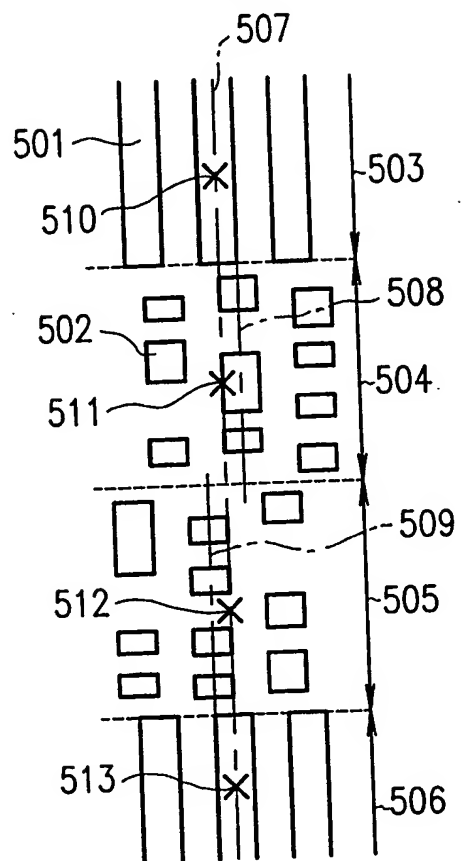


FIG. 20



**FIG. 21A****FIG. 21B****FIG. 21C**

**FIG. 22A**



**FIG. 22B**

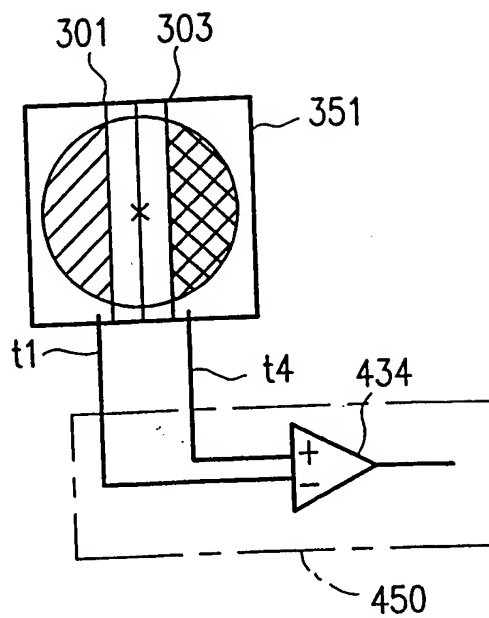


FIG. 23A

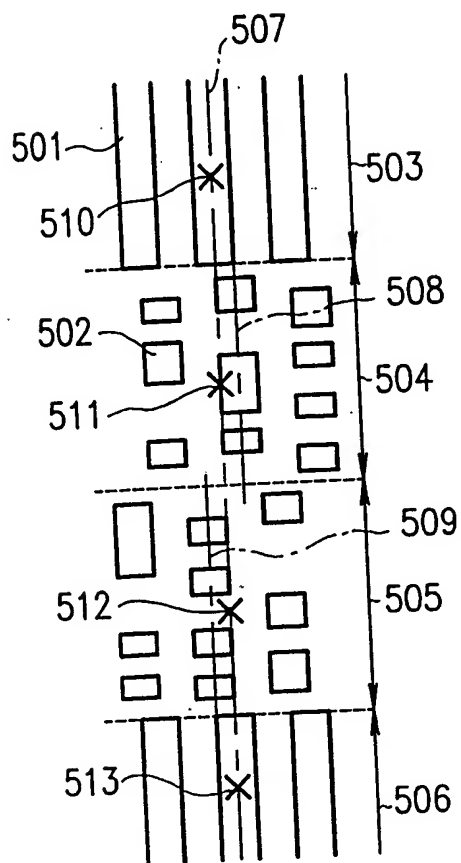


FIG. 23B

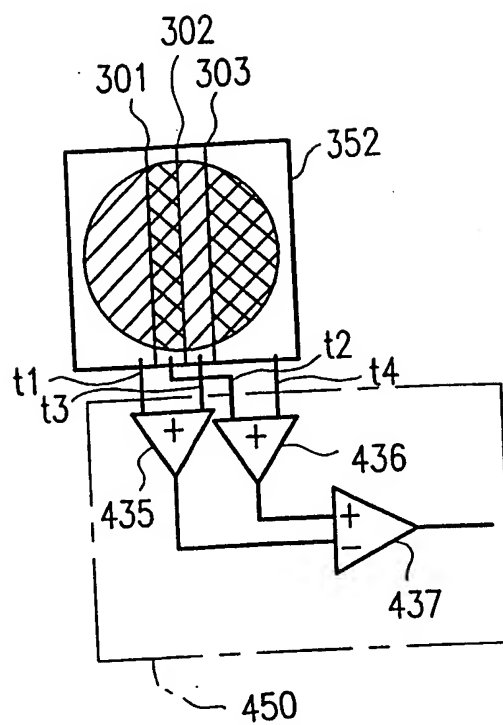
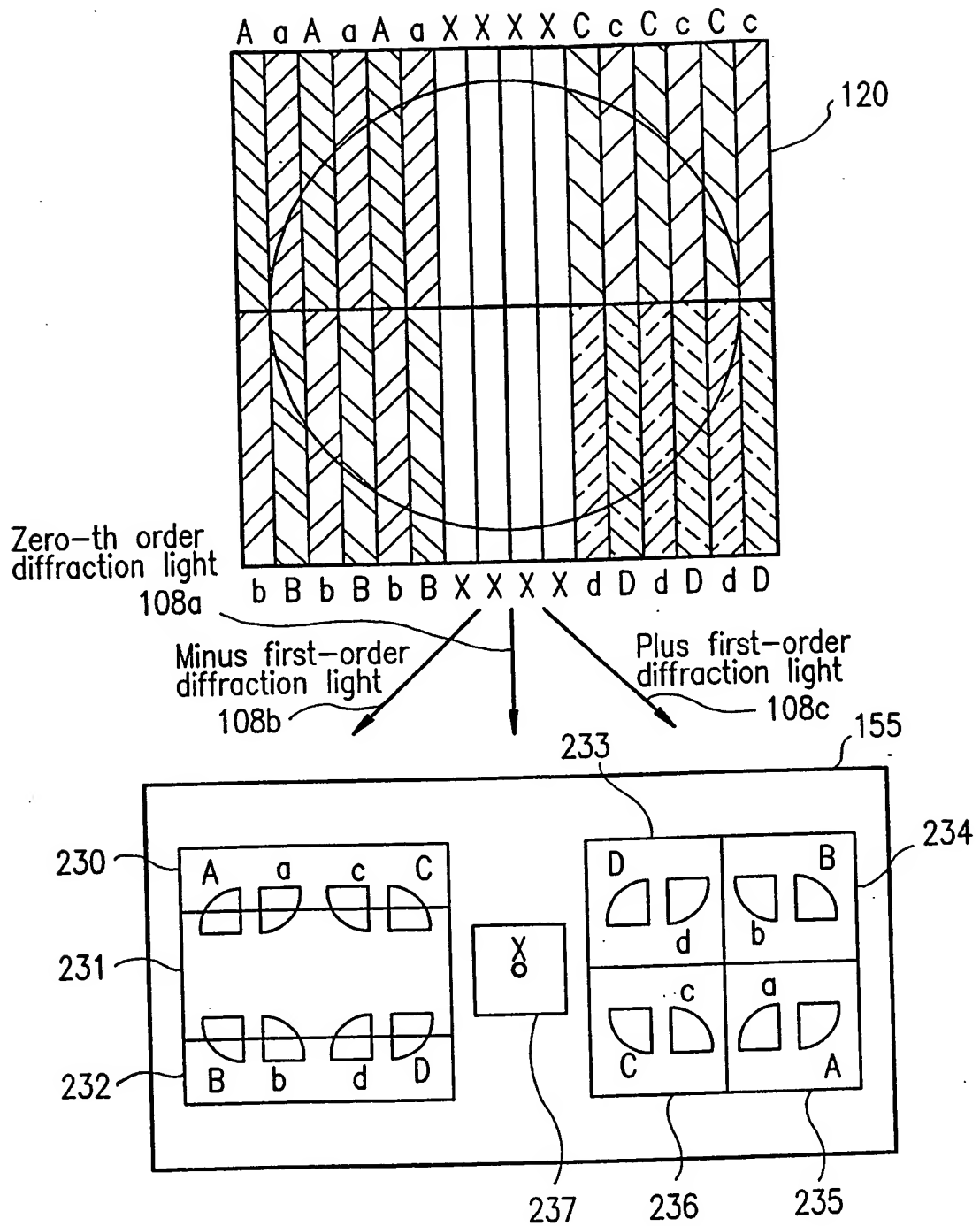


FIG. 24



**FIG. 25**

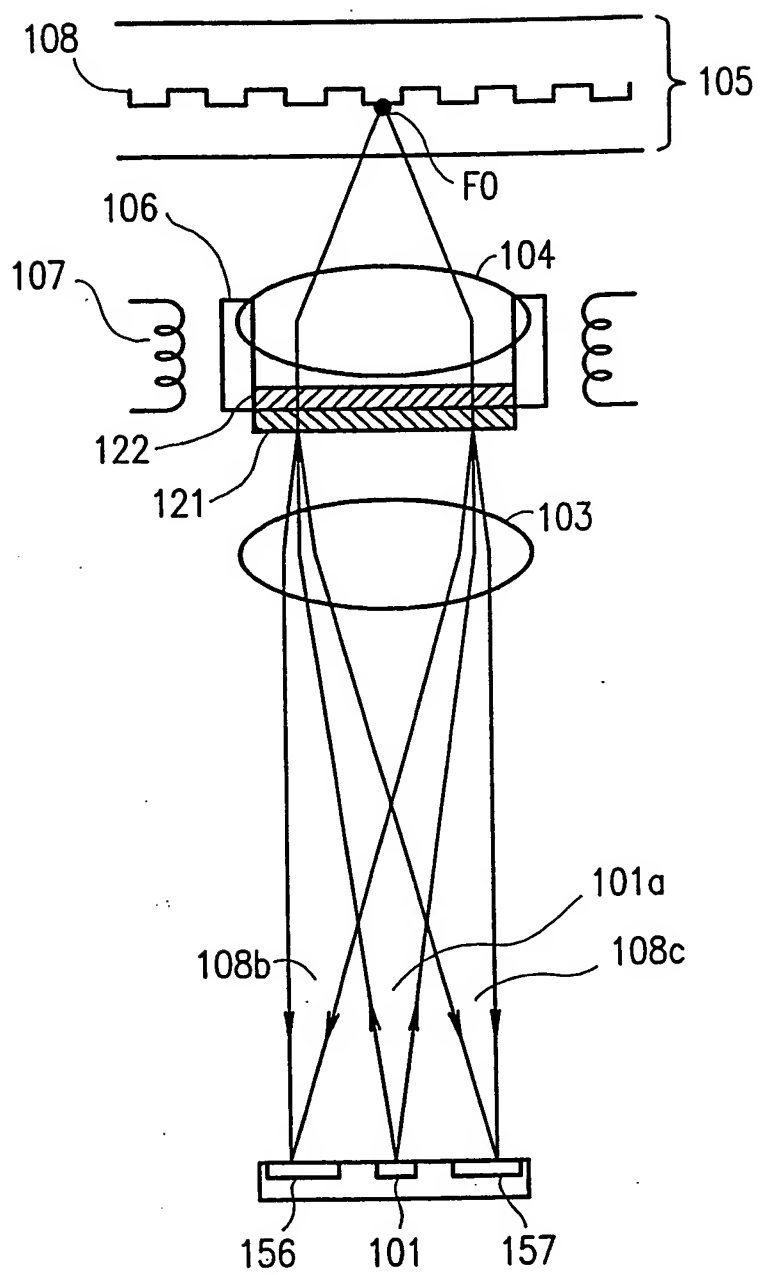
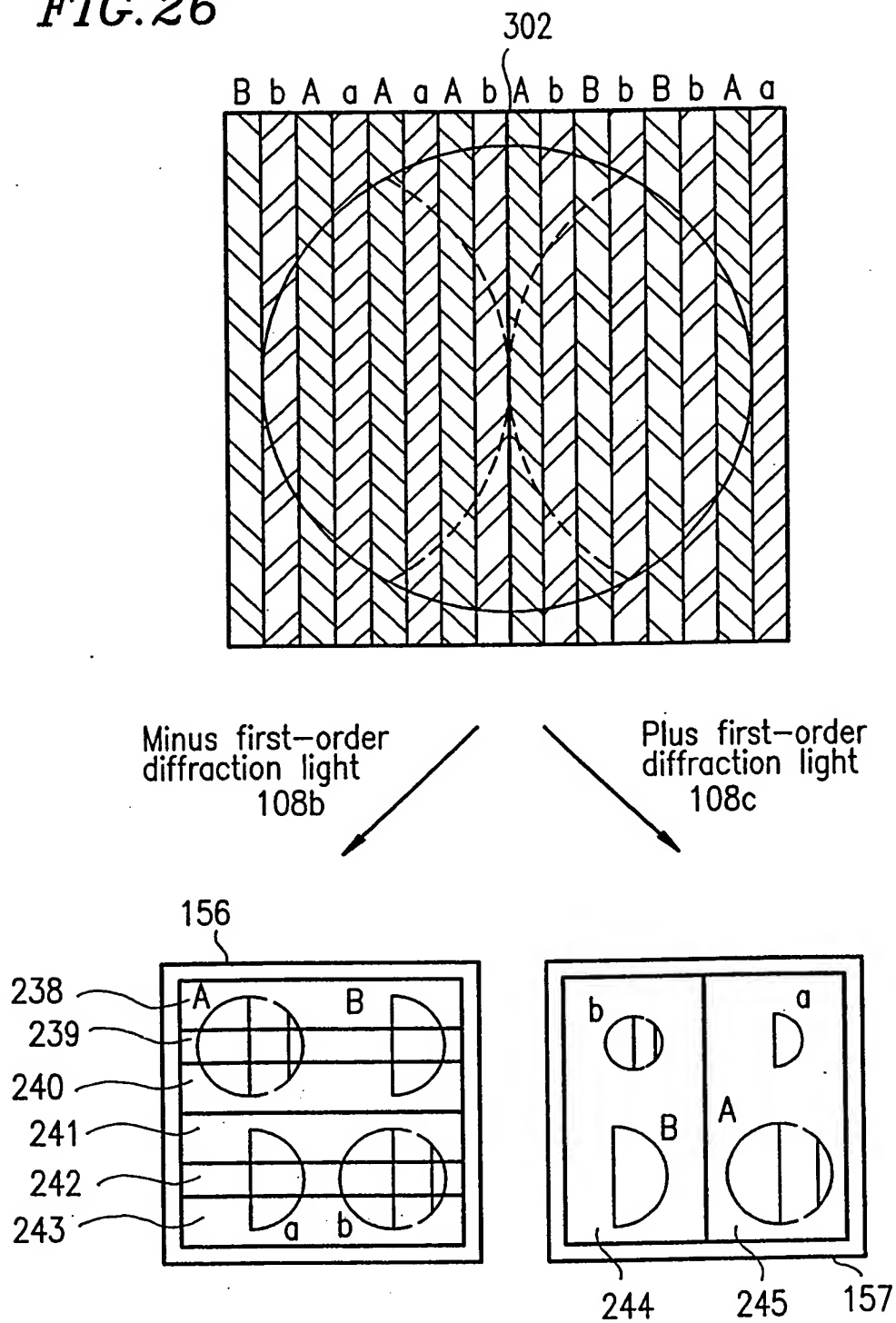


FIG. 26



*FIG. 27*

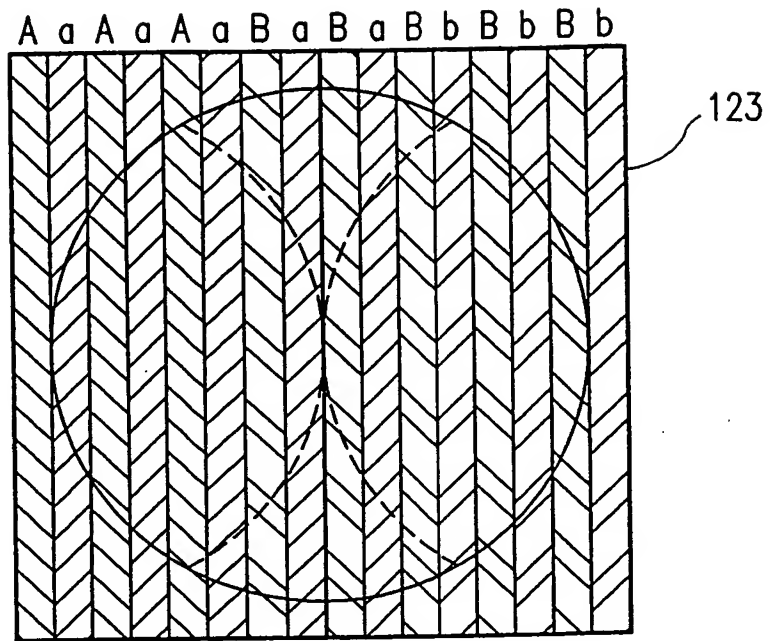
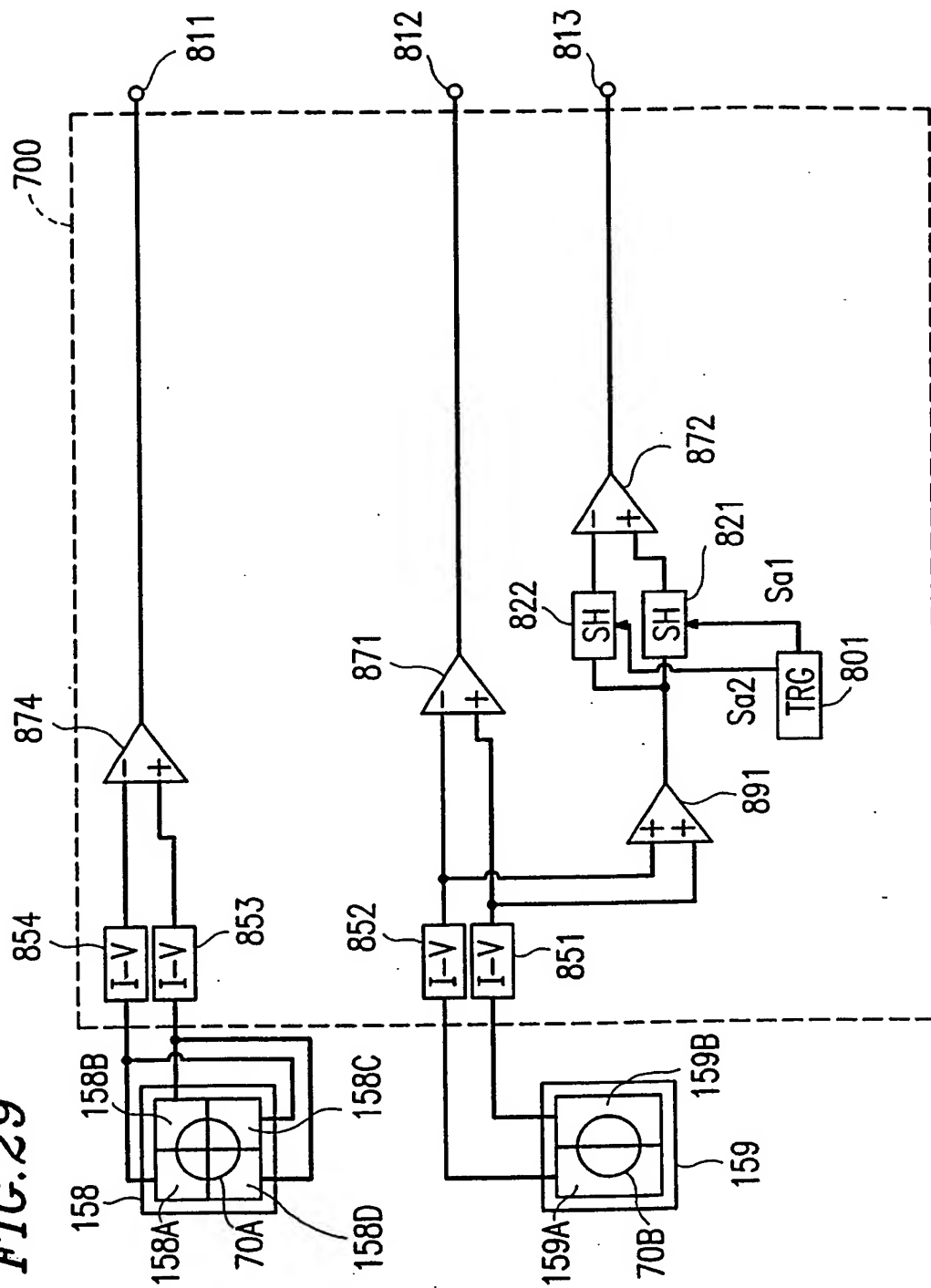


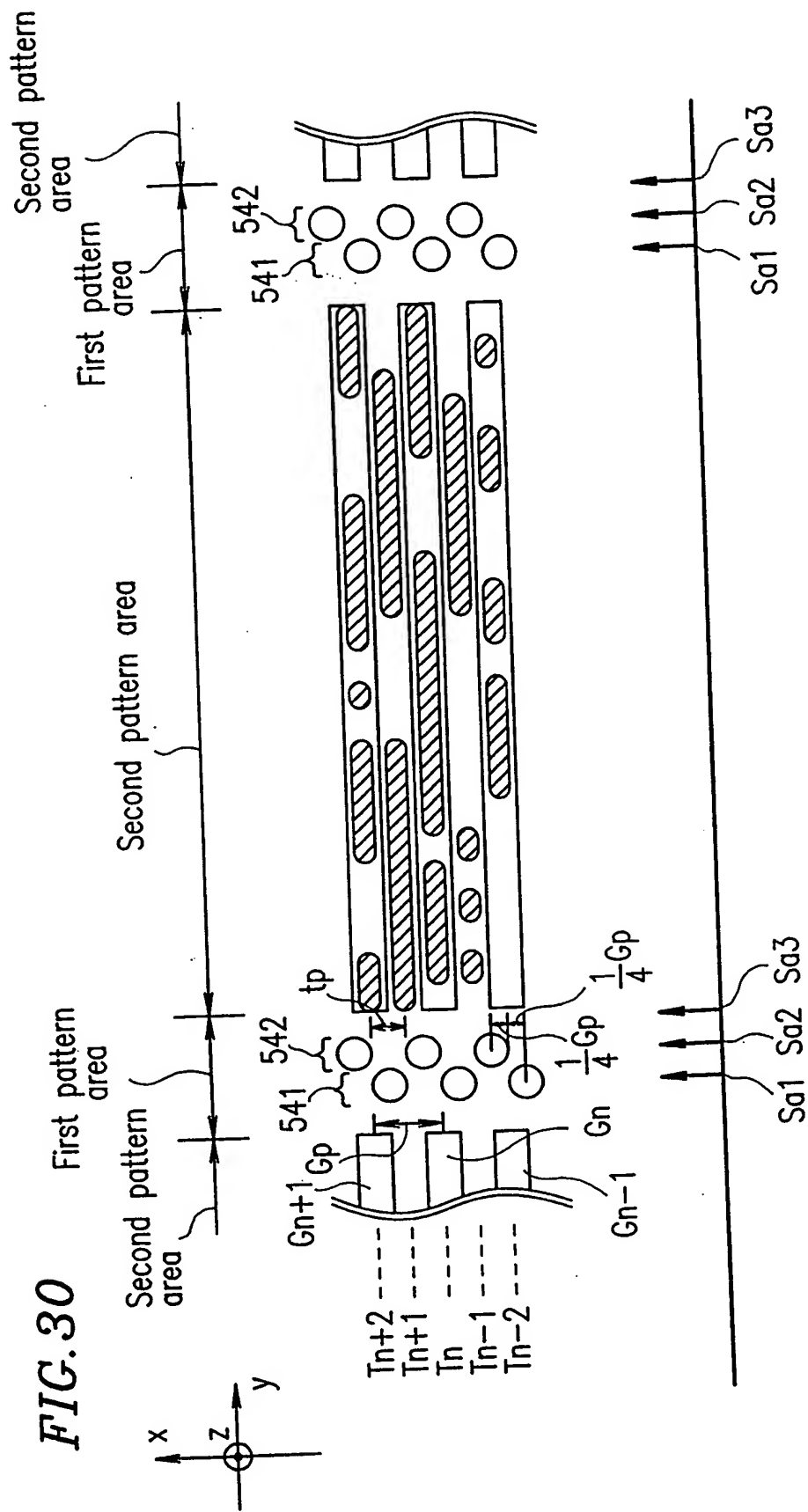




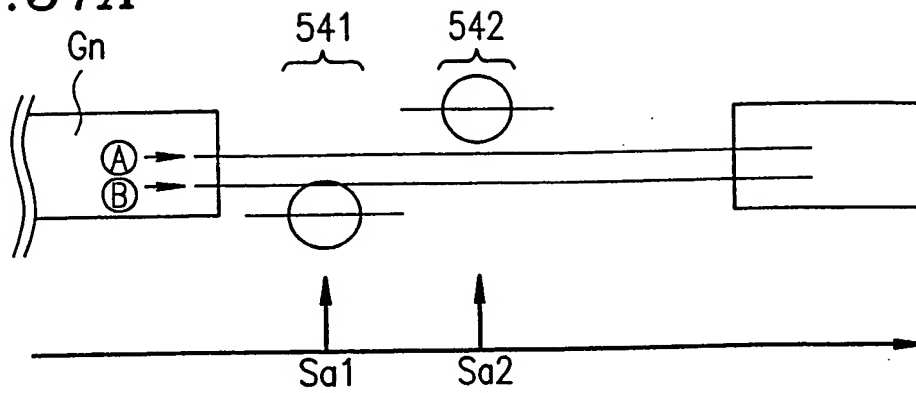
FIG. 29



**FIG. 30**

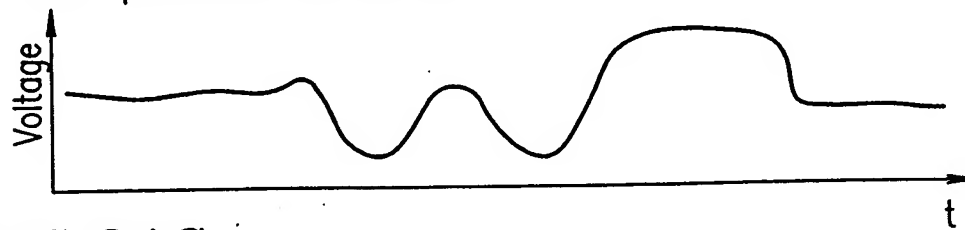


**FIG. 31A**



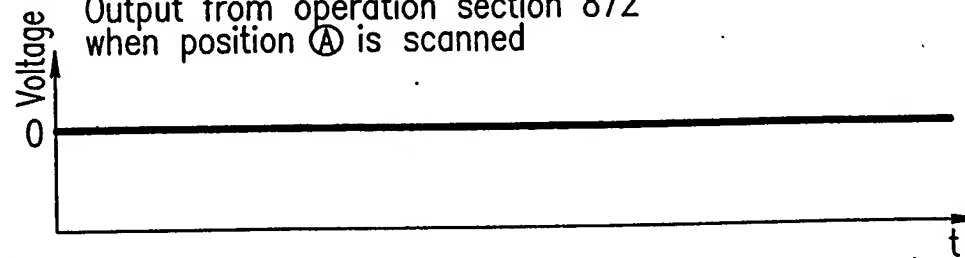
**FIG. 31B**

Output from adder section 891  
when position ① is scanned



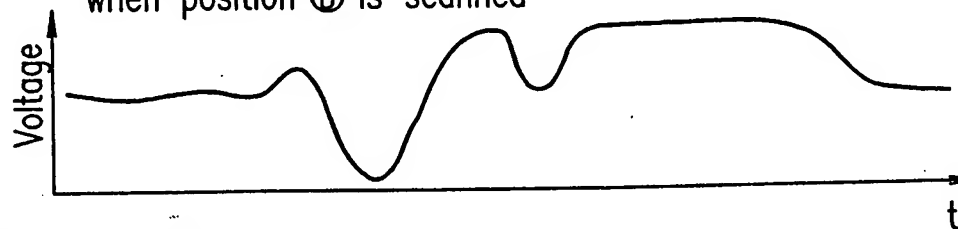
**FIG. 31C**

Output from operation section 872  
when position ① is scanned



**FIG. 31D**

Output from adder section 891  
when position ② is scanned



**FIG. 31E**

Output from operation section 872  
when position ② is scanned

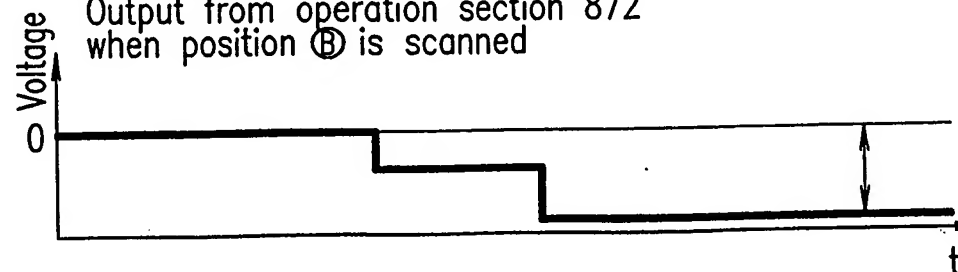
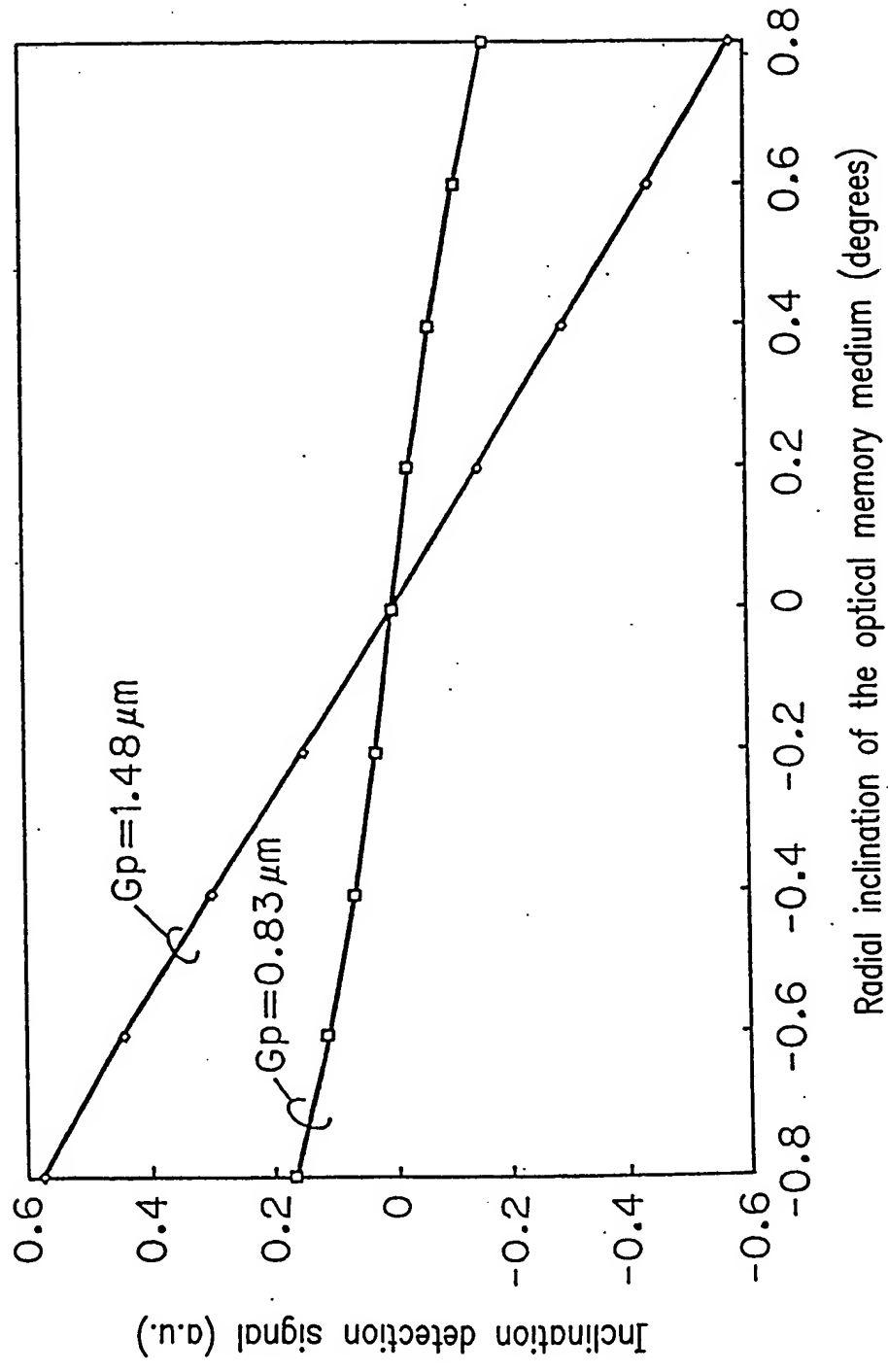


FIG.32



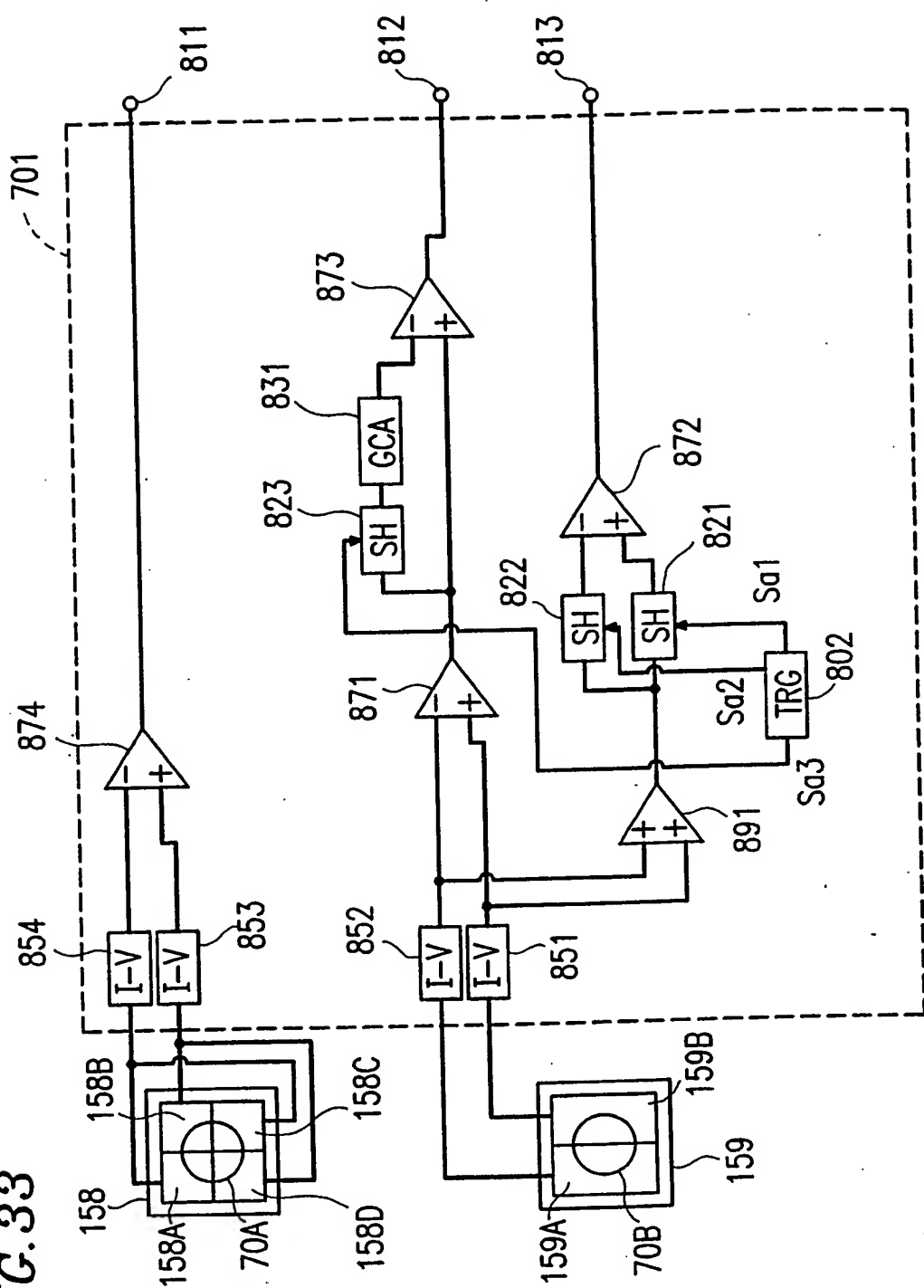


FIG. 34

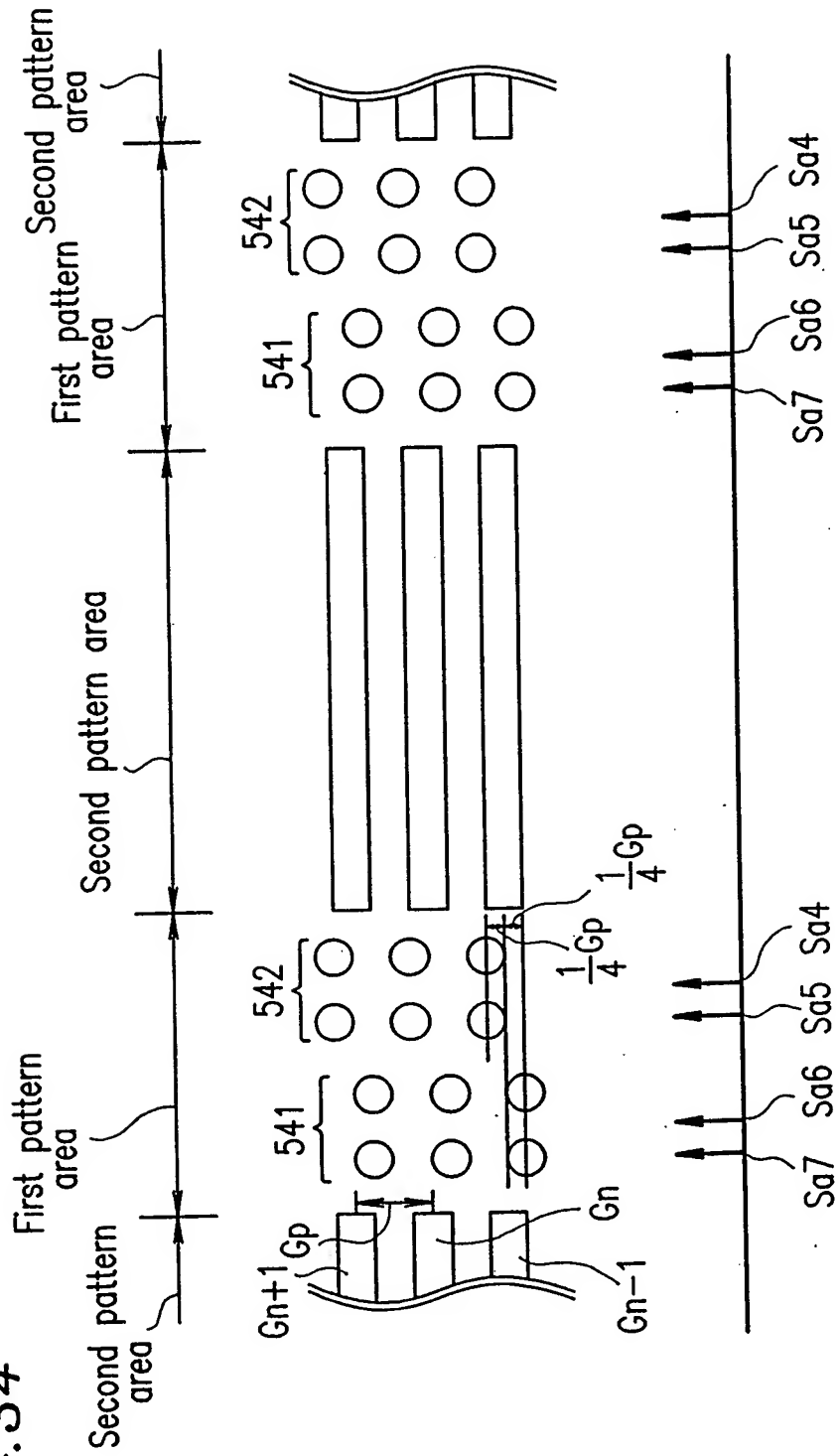
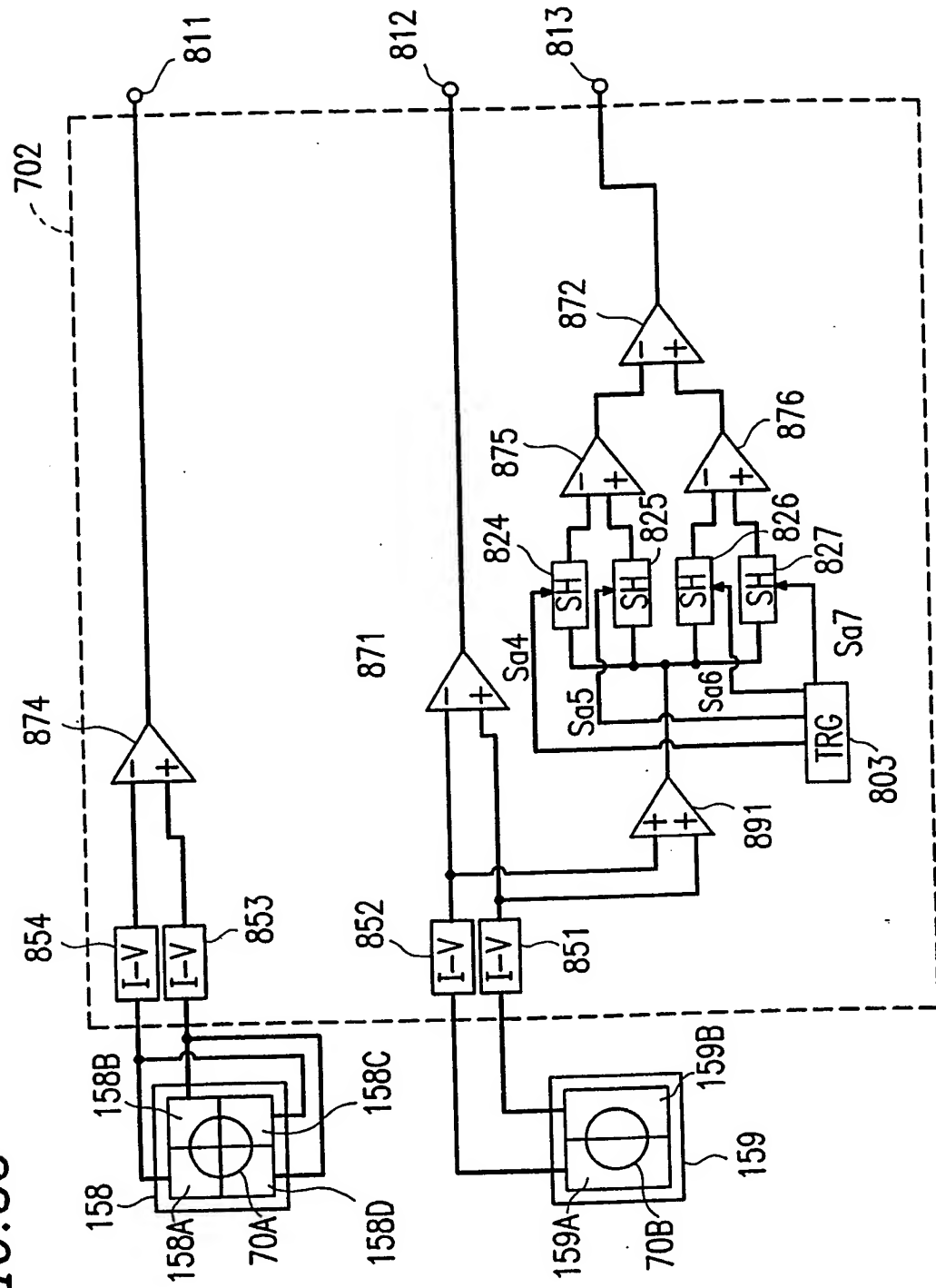
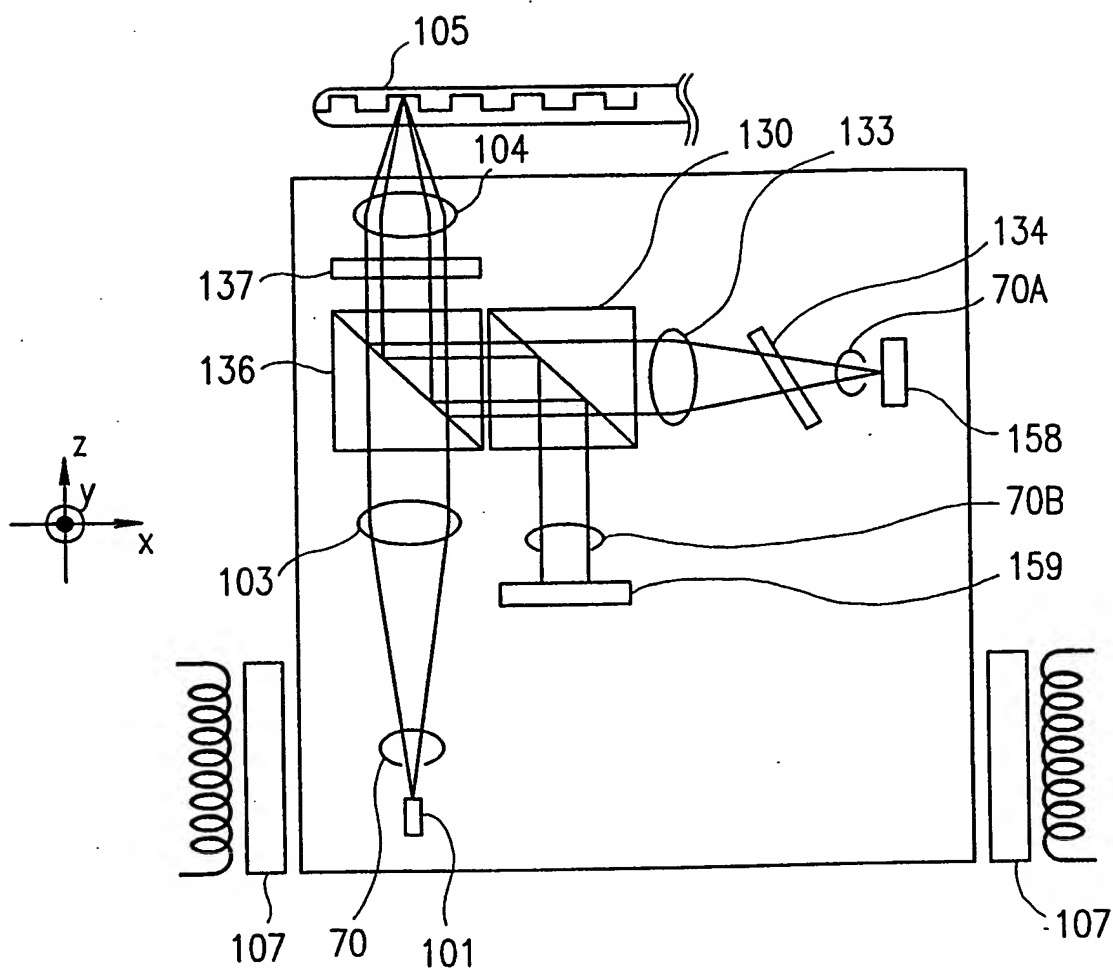


FIG. 35





**FIG. 36**



*FIG. 37*

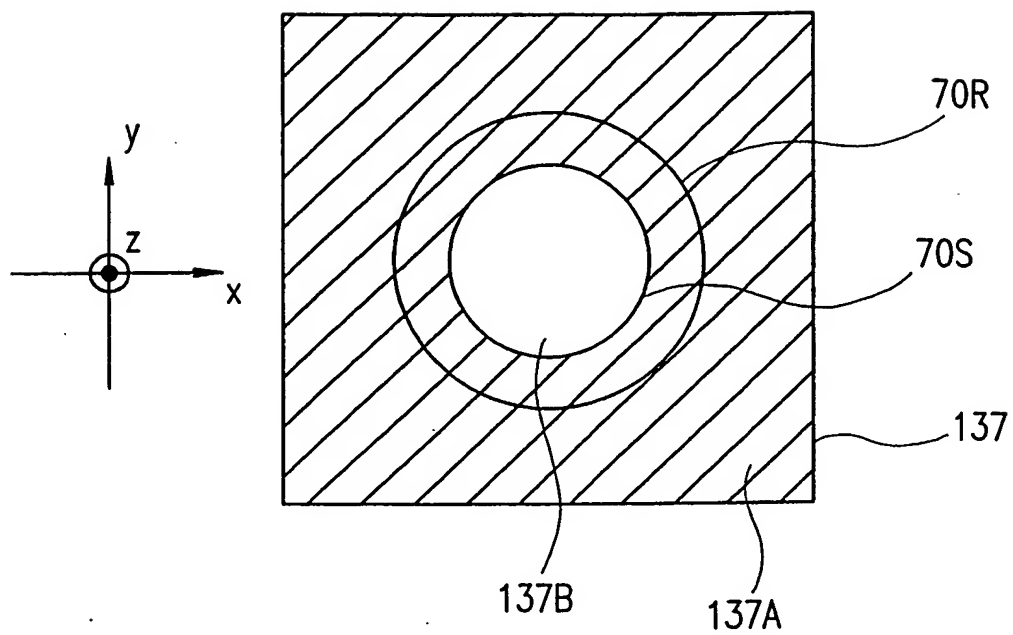
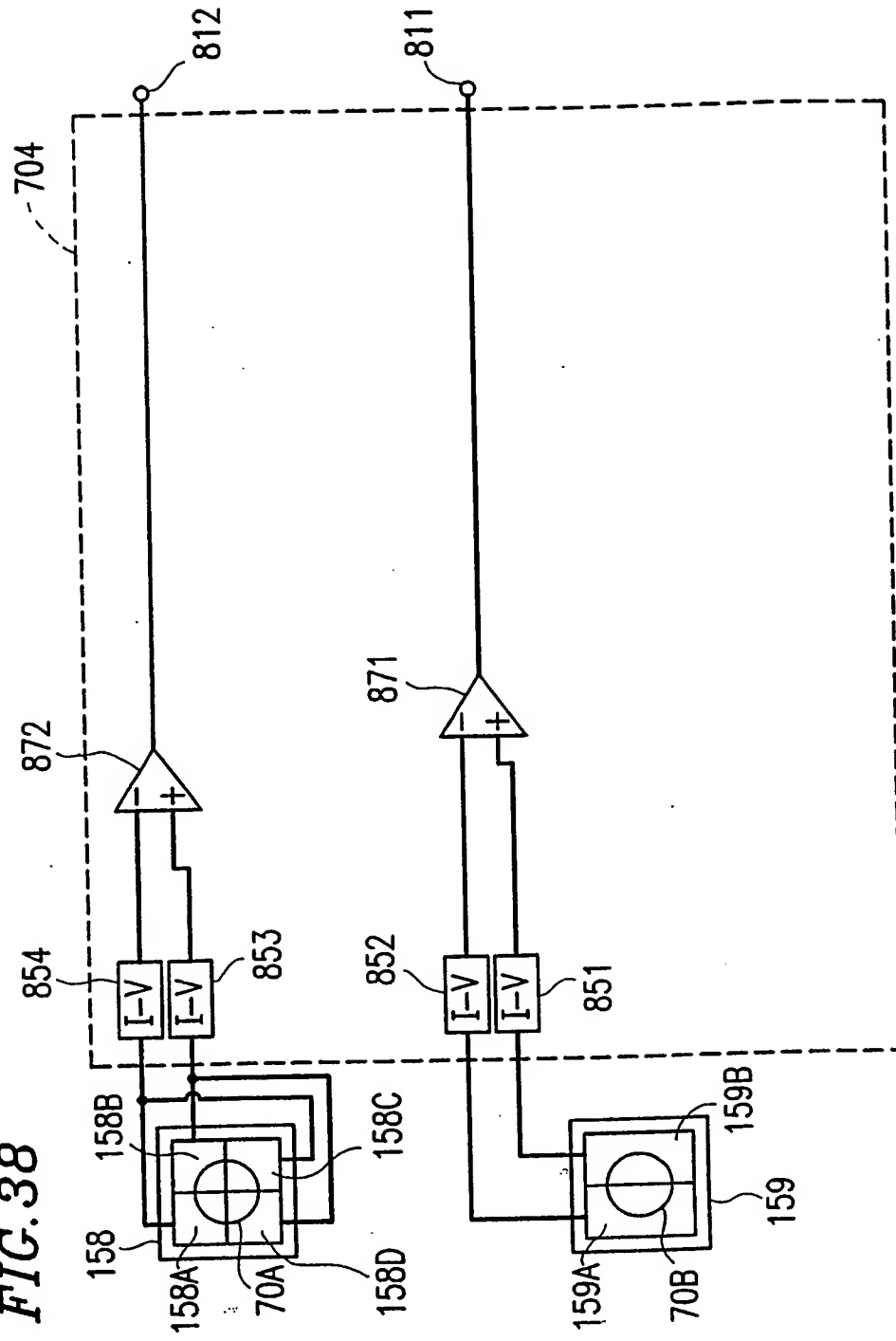
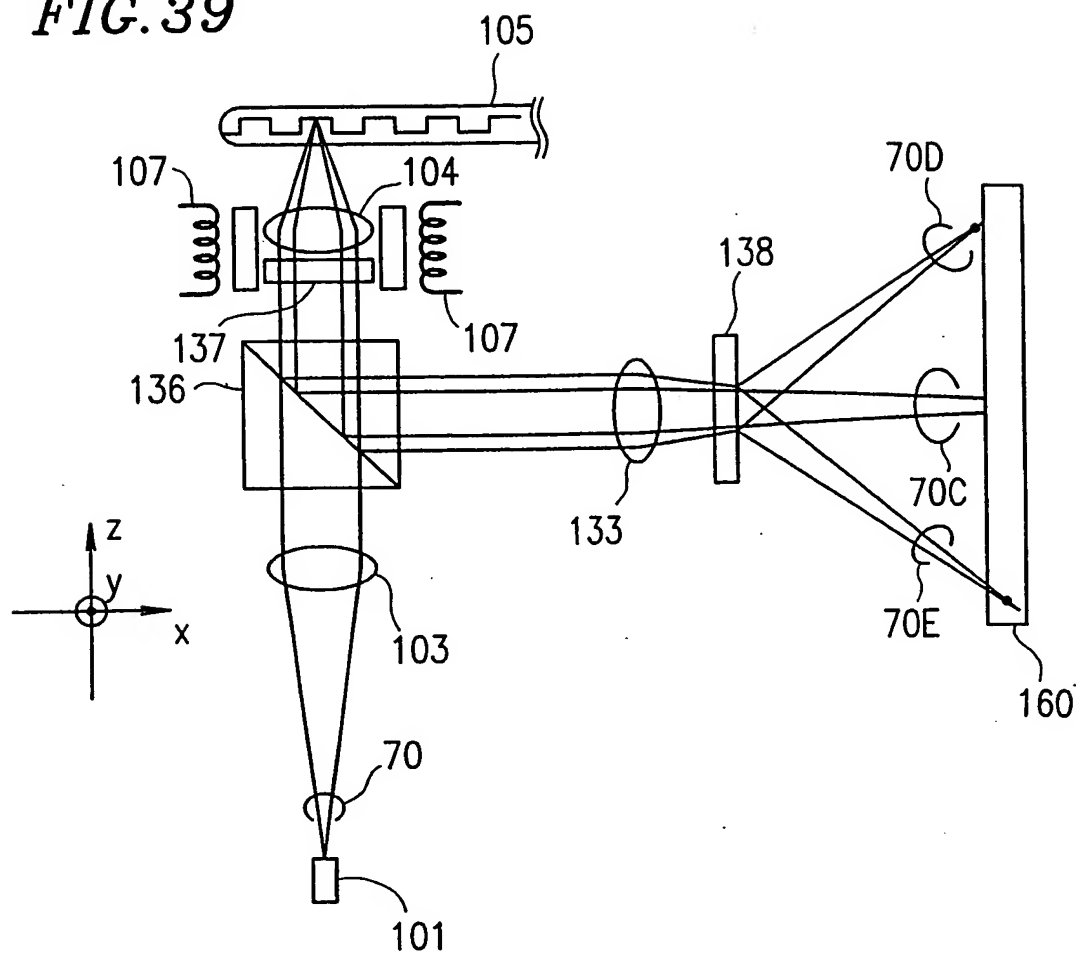


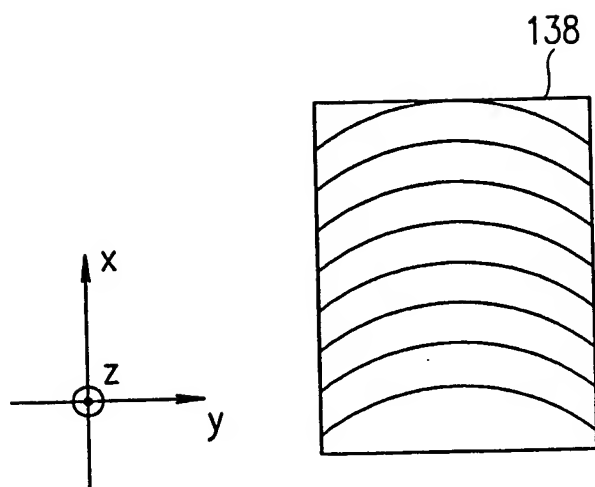
FIG. 38



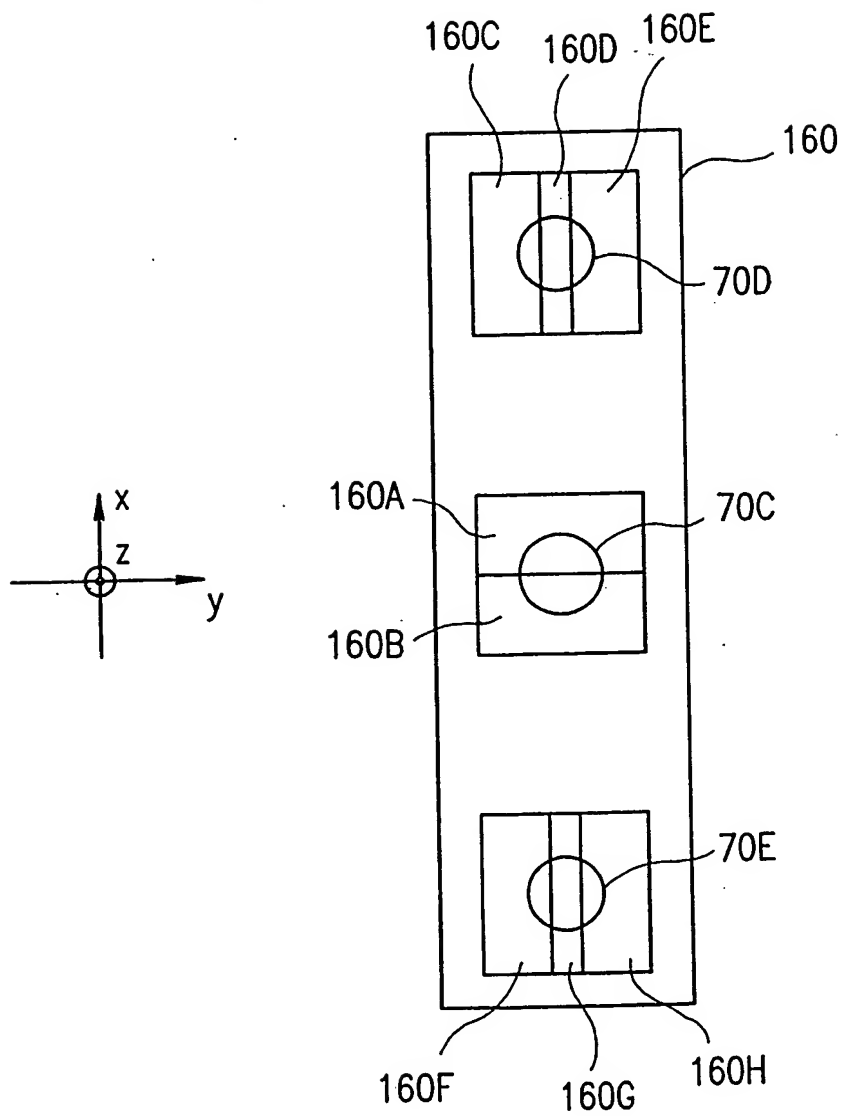
**FIG. 39**



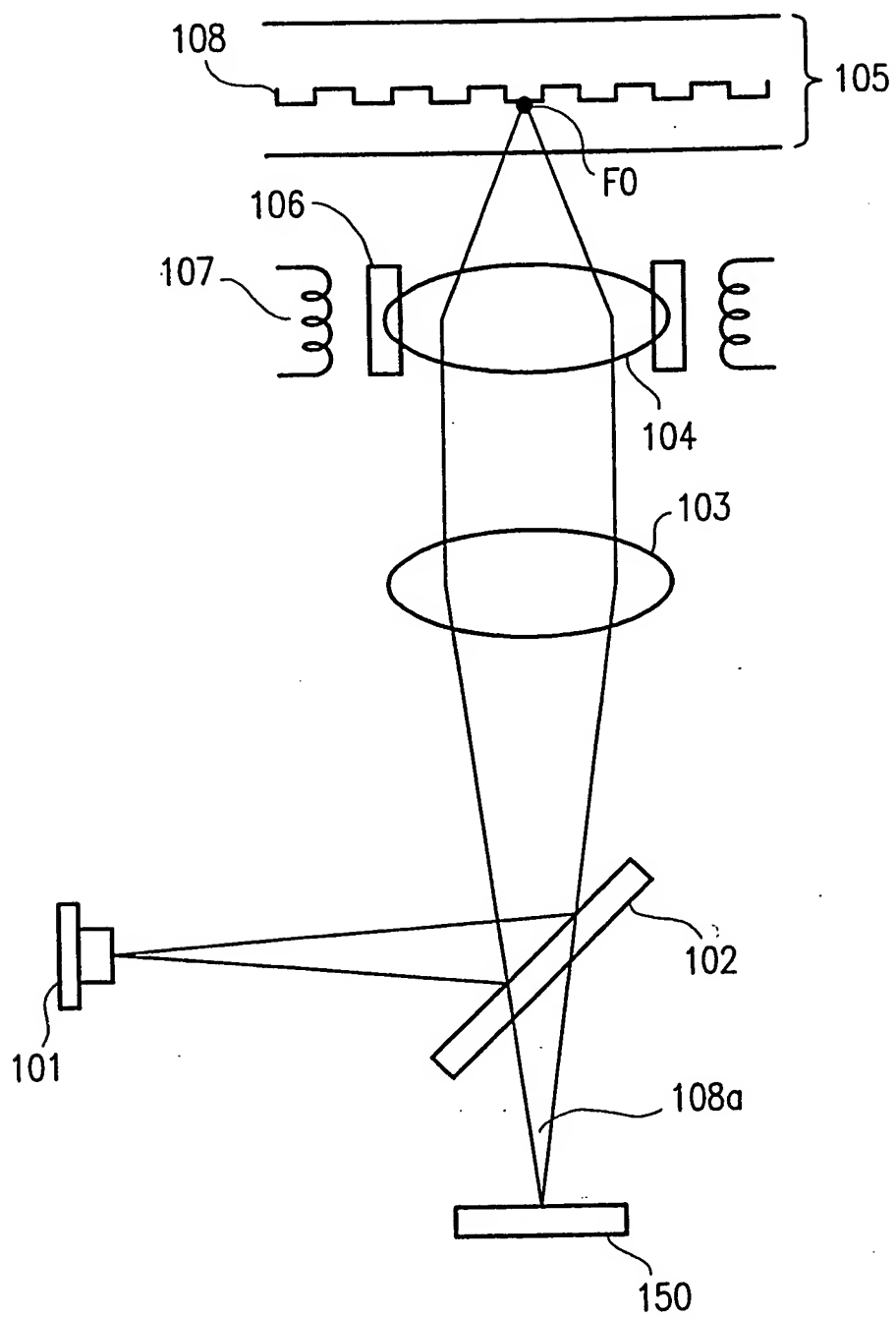
**FIG. 40**



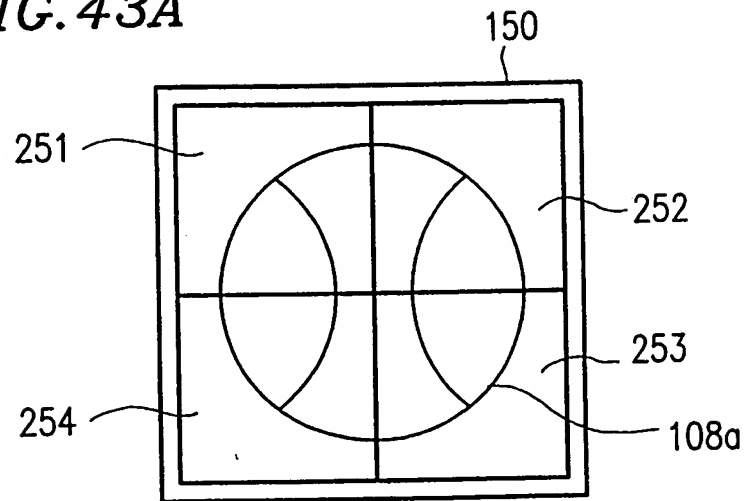
**FIG. 41**



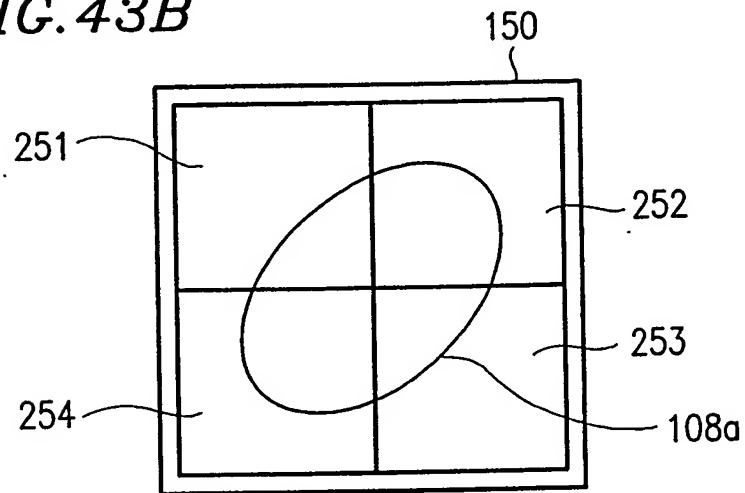
*FIG. 42*



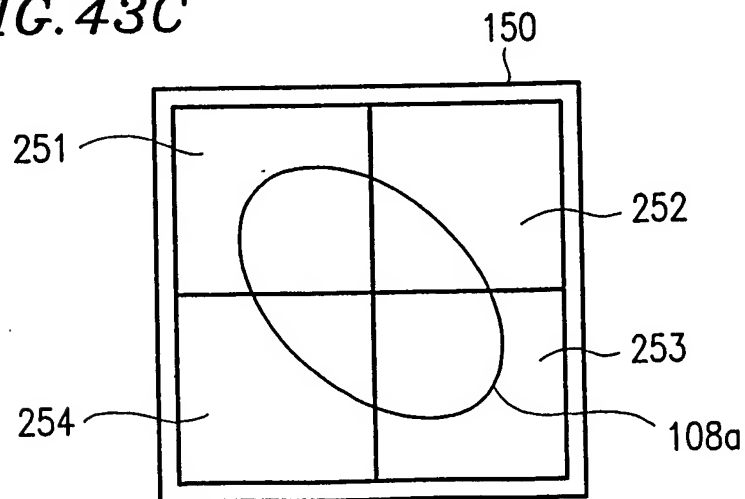
**FIG. 43A**



**FIG. 43B**



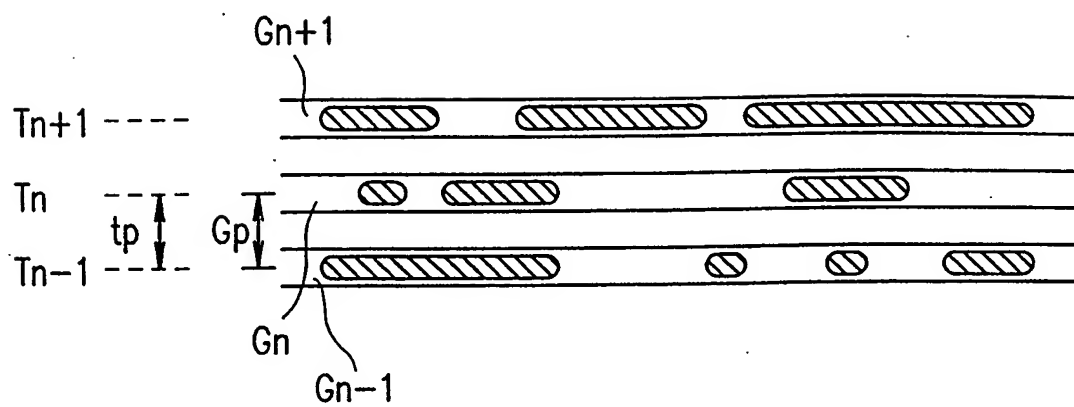
**FIG. 43C**



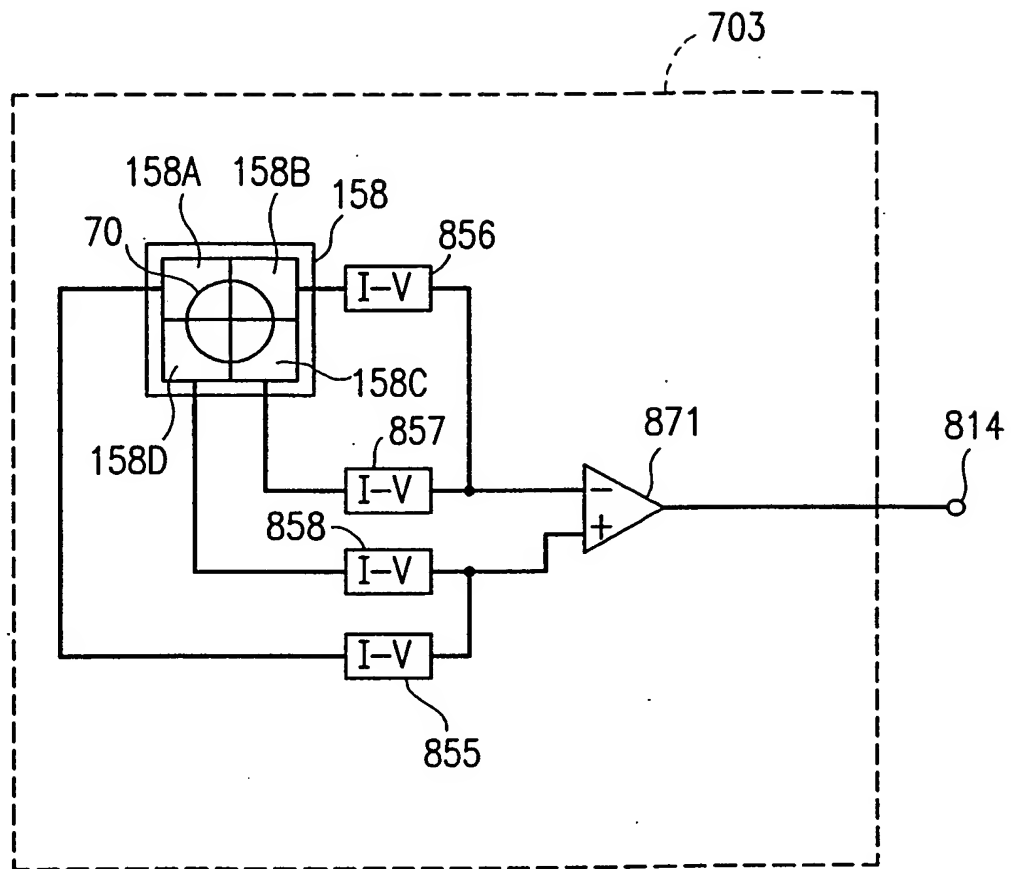




**FIG. 45**



**FIG. 46**



**FIG. 47**

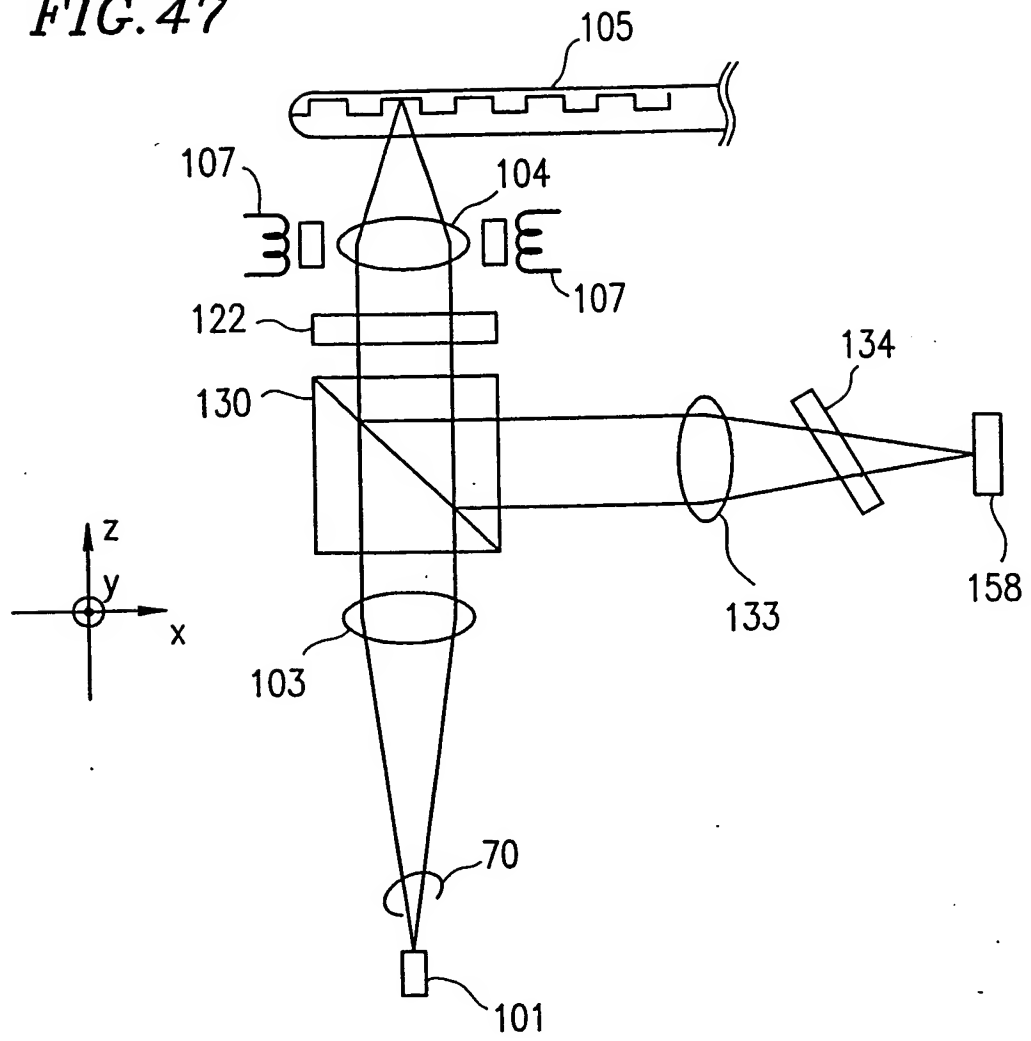


FIG. 48

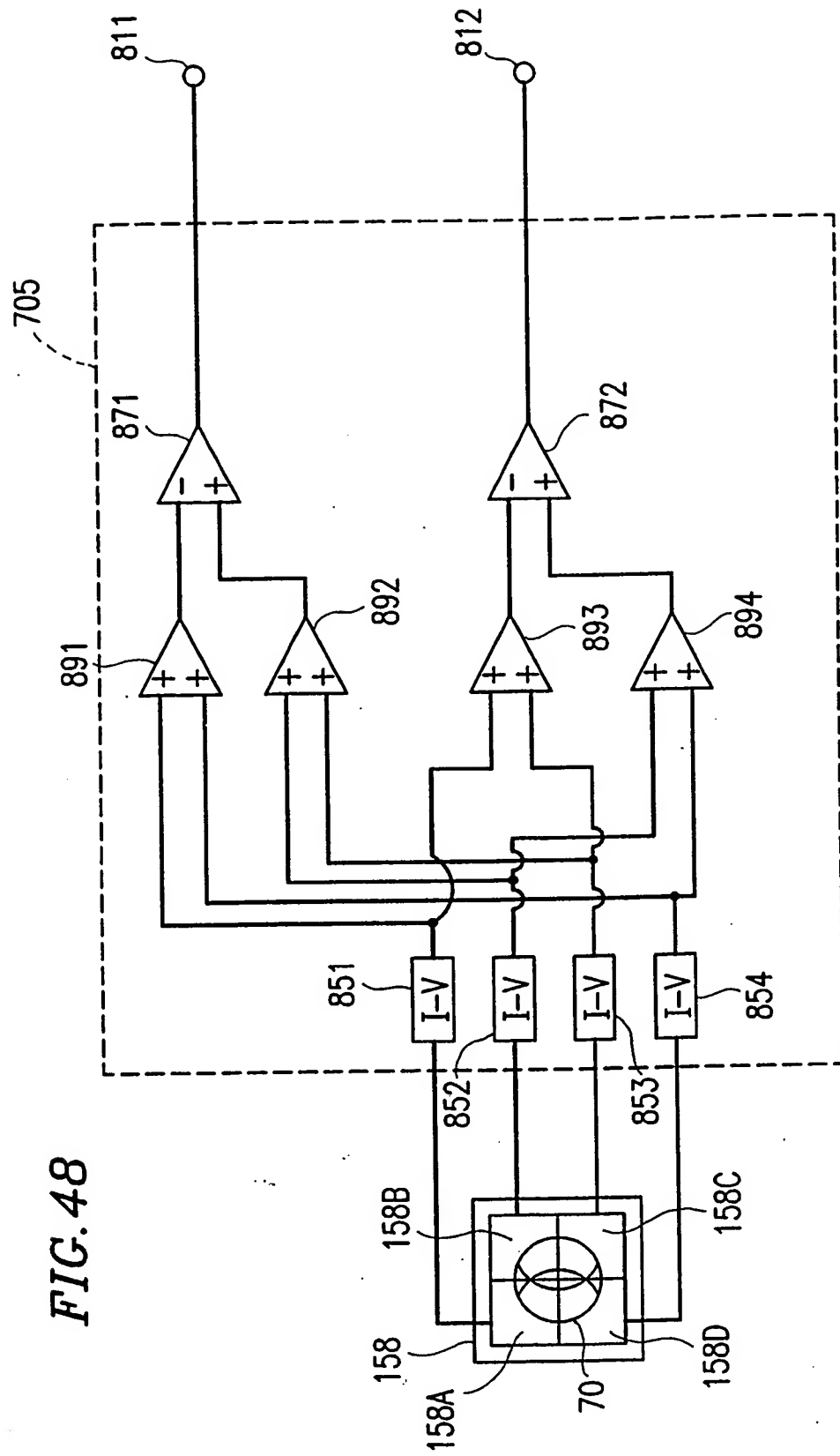
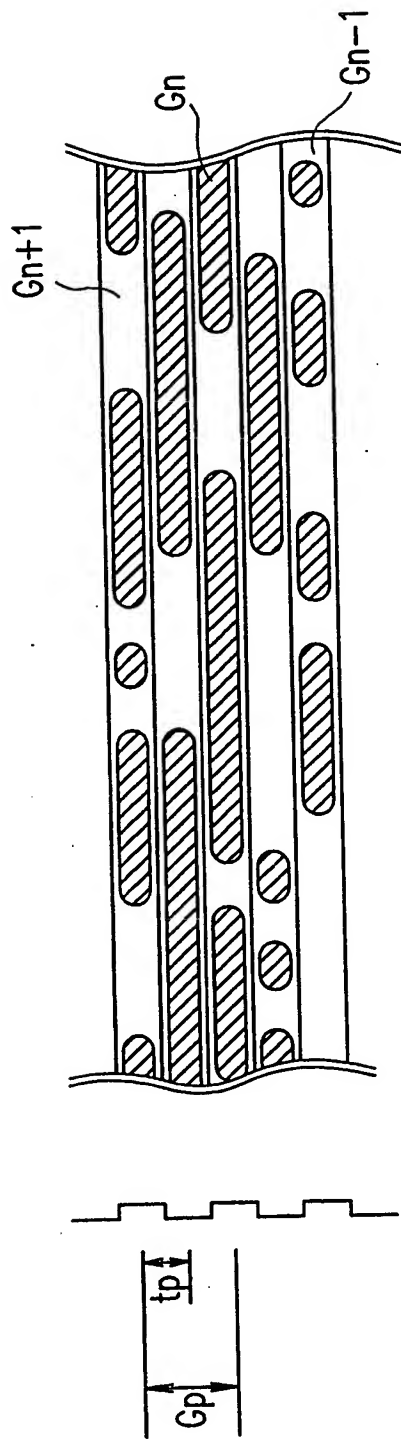
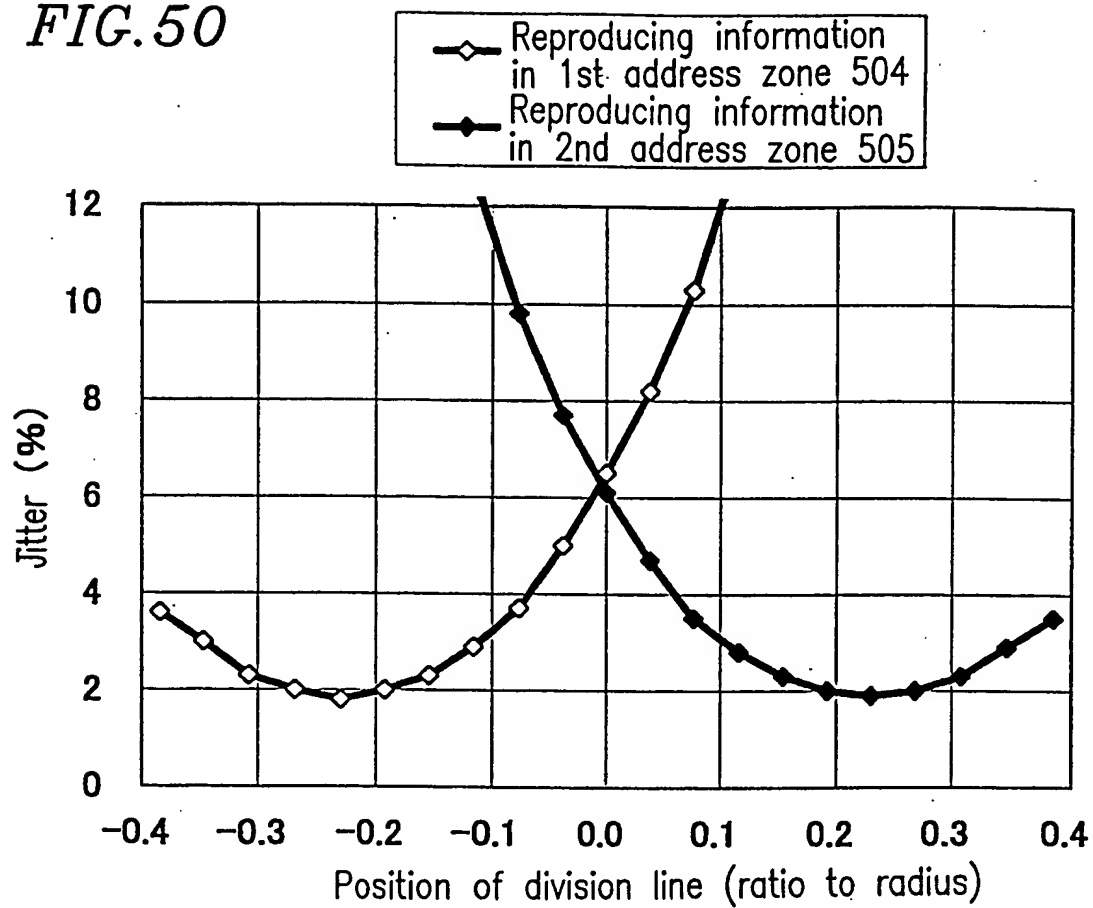


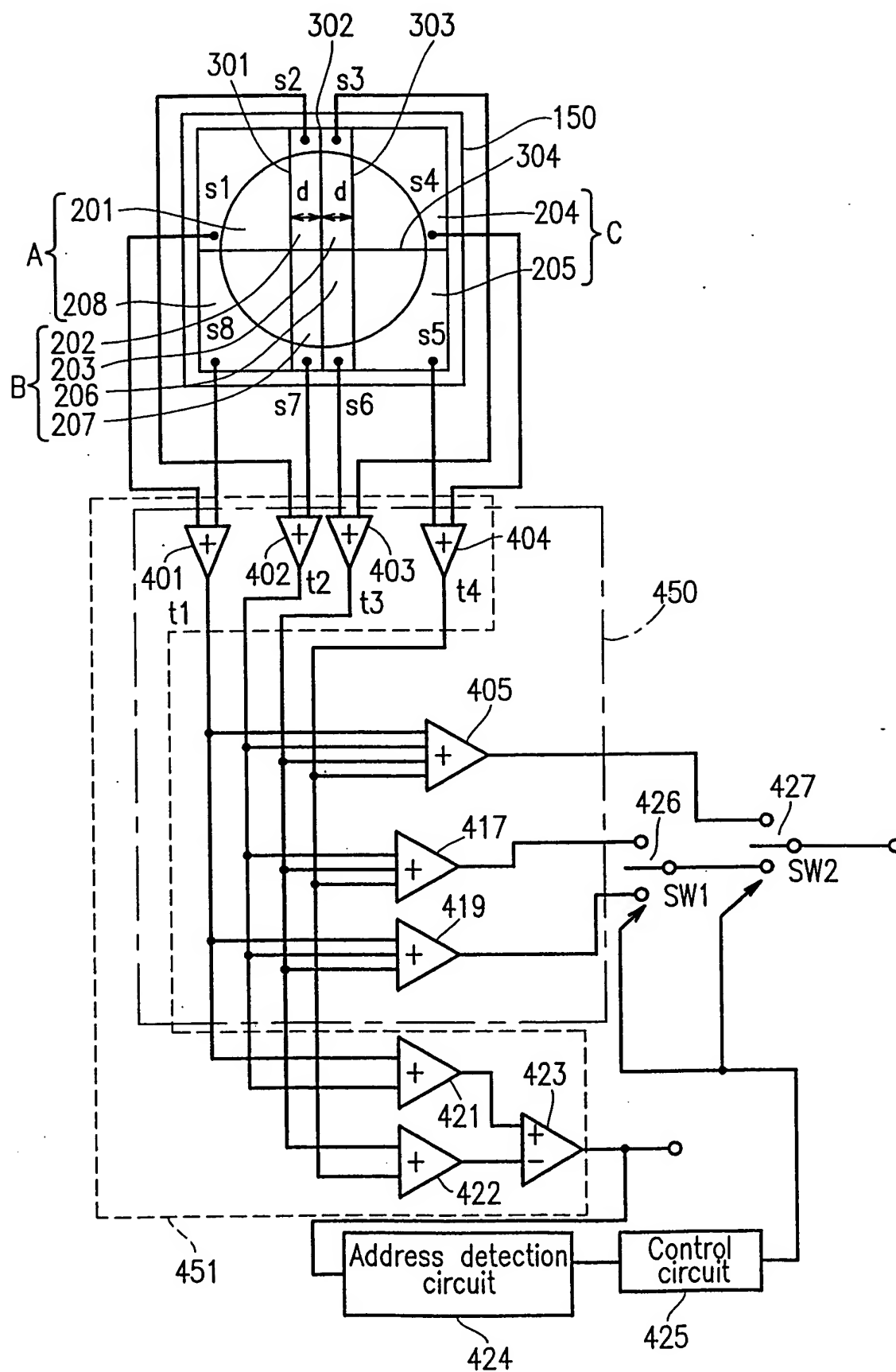
FIG. 49



*FIG. 50*

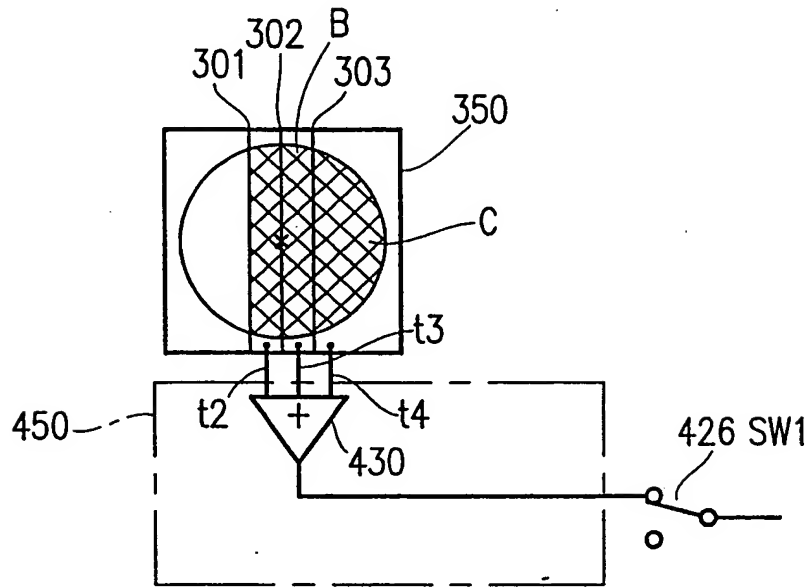


**FIG. 51**

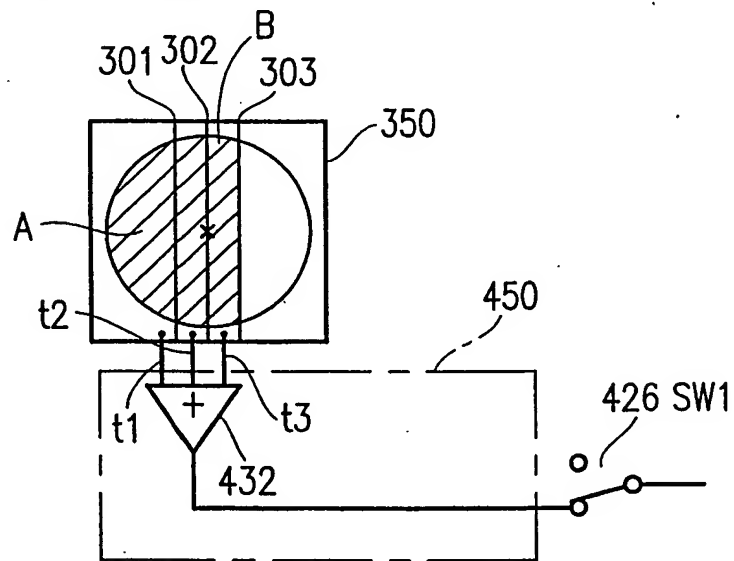




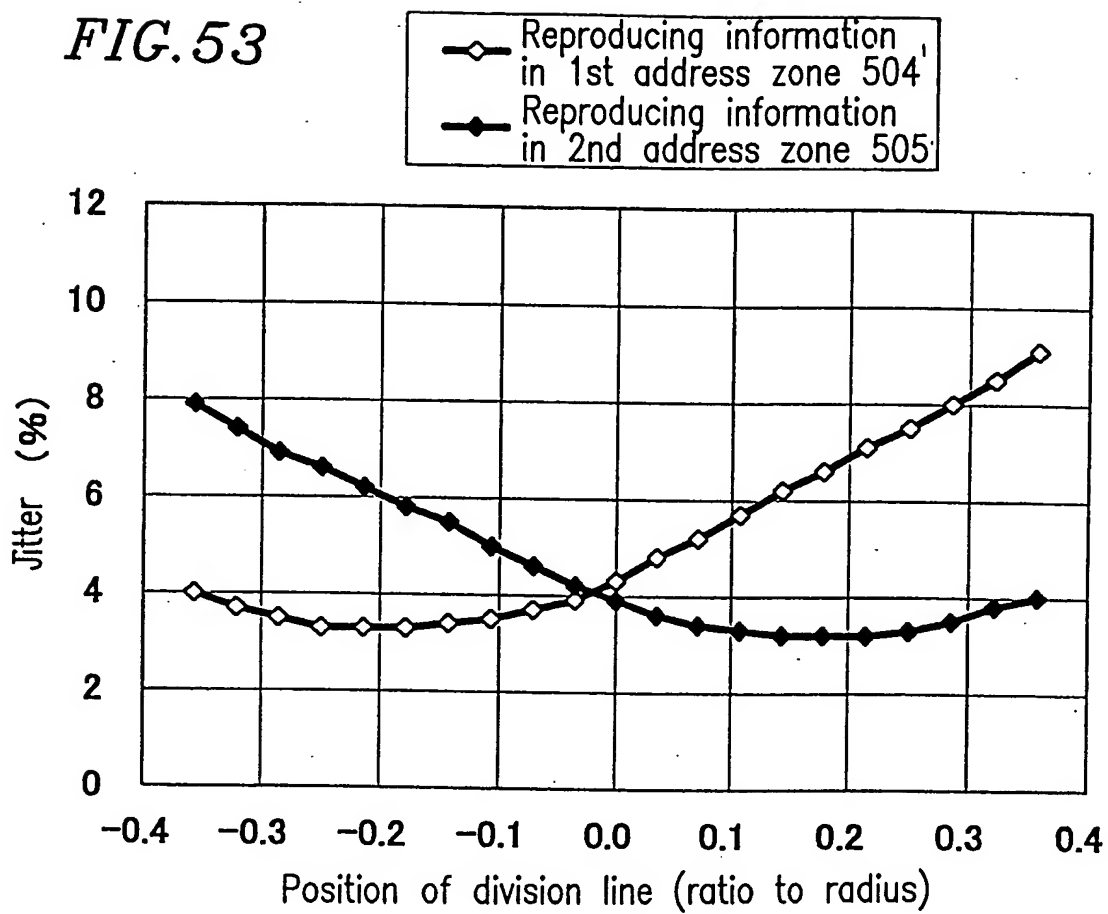
**FIG. 52A**



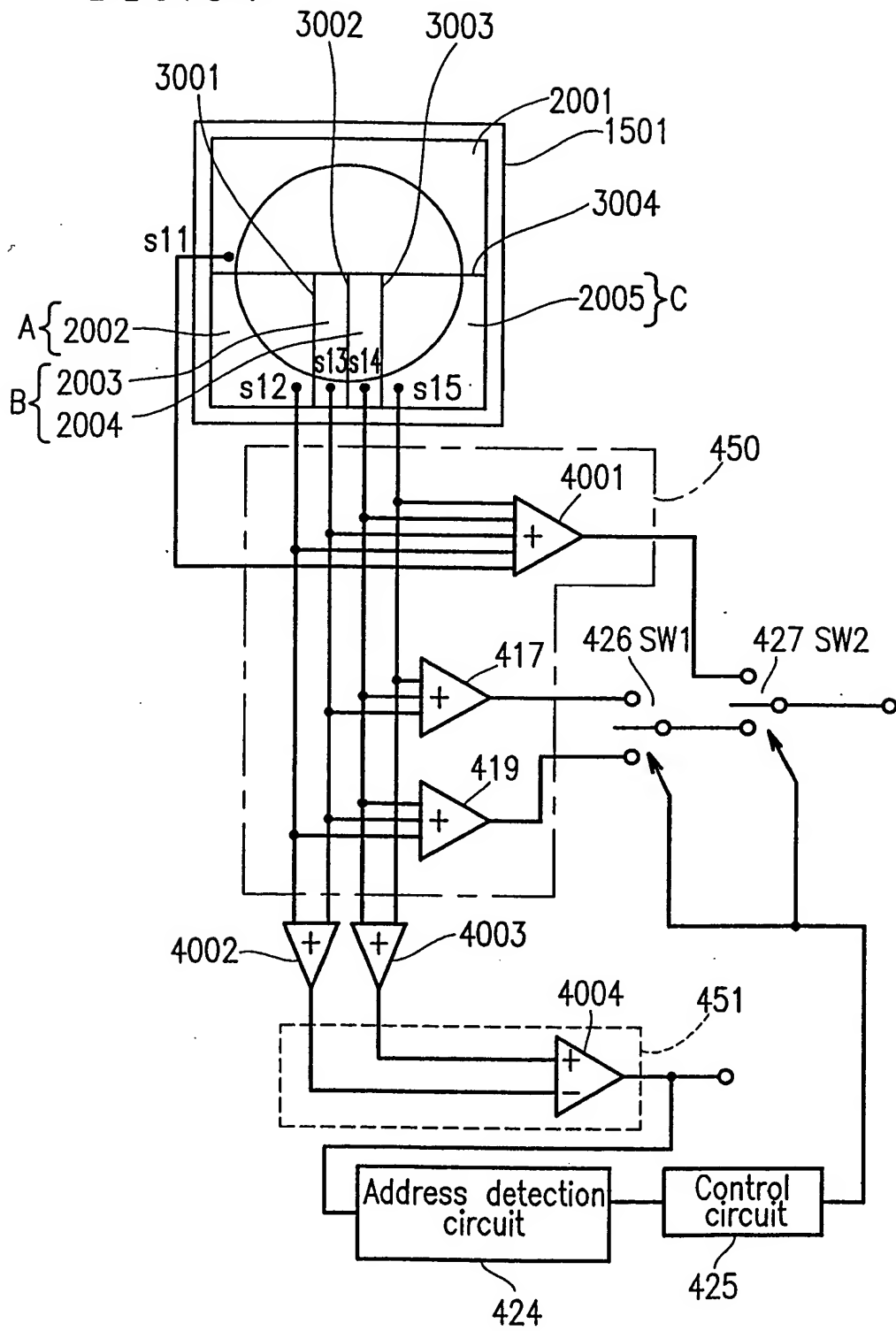
**FIG. 52B**



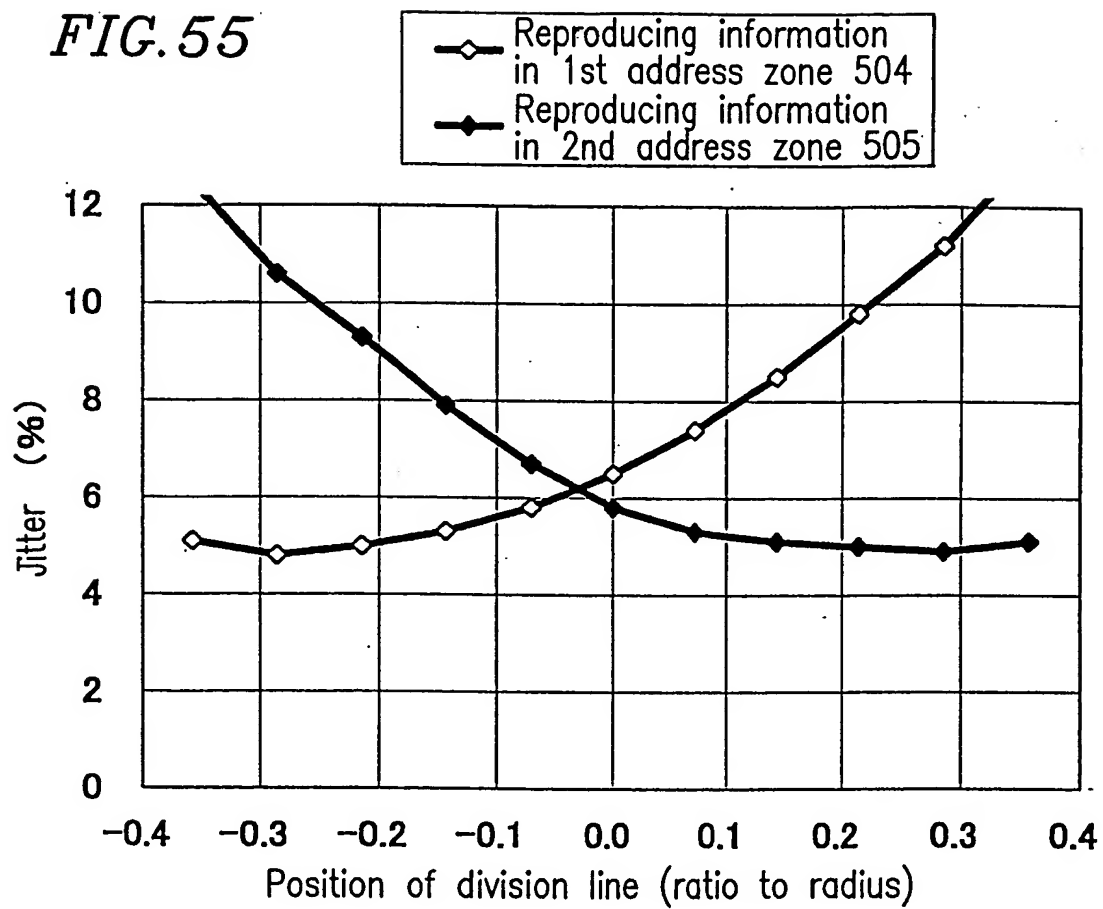
*FIG. 53*



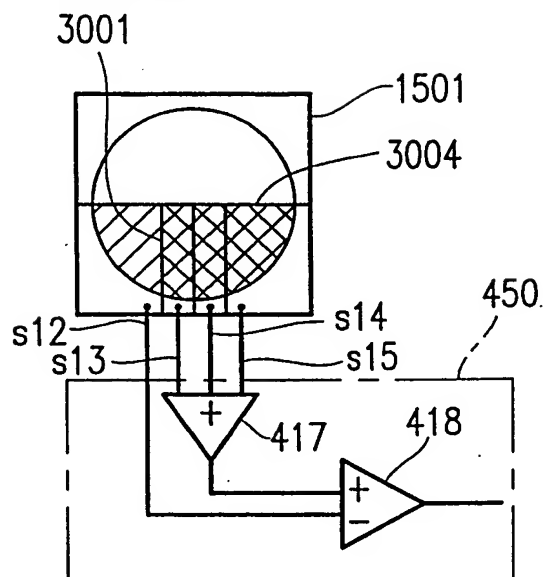
**FIG. 54**



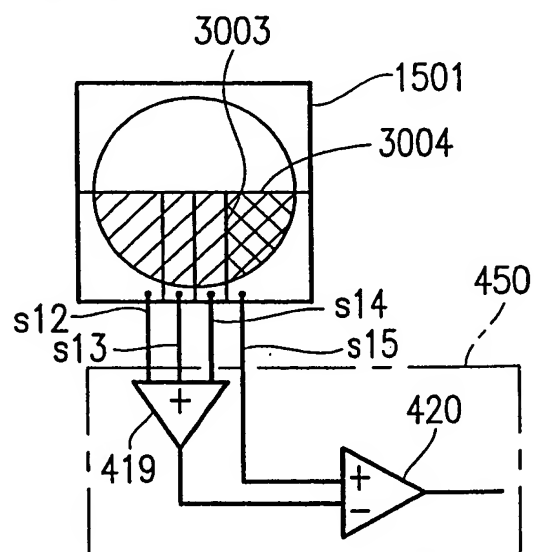
*FIG. 55*



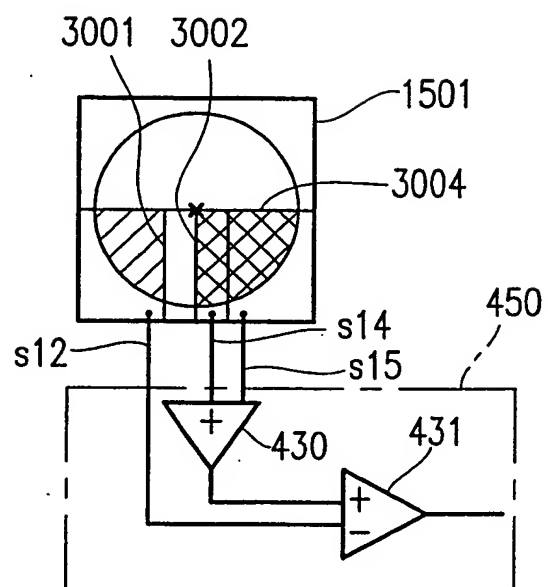
**FIG. 56A**



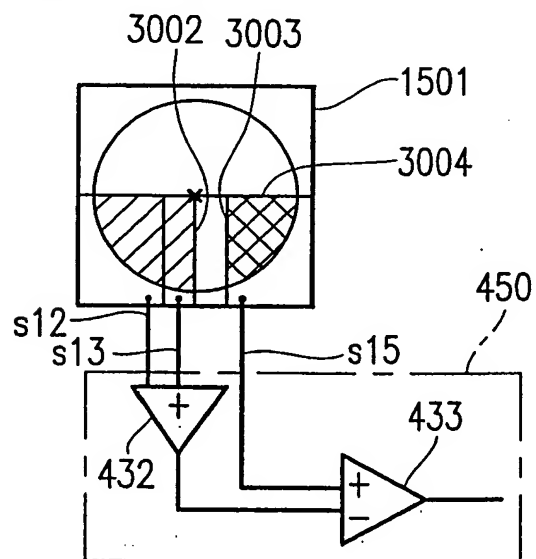
**FIG. 56B**



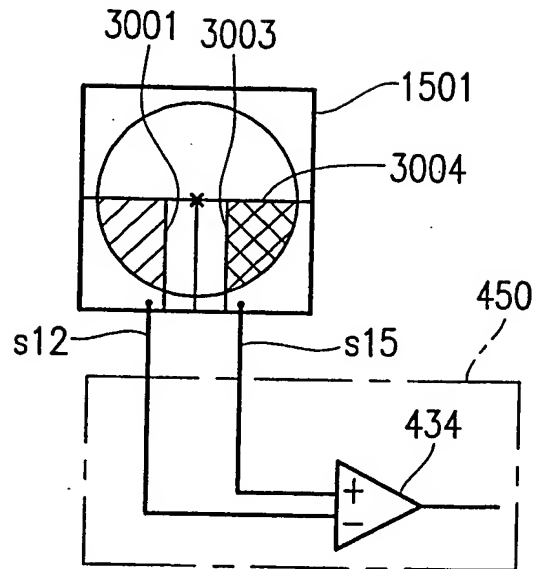
**FIG. 57A**



**FIG. 57B**



**FIG. 58**



**FIG. 59**

